
BIO DATA

Dr.M Venu Gopala Rao,

*B.E., M.E. (Elect. Power Engg.), M.Tech. (Comp. Sci.), Ph.D (EEE),
Fellow IE(I), Senior Member IEEE, Life Member SESI, Life Member ISTE*

Professor & Head

**Department of Electrical & Electronics Engineering,
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Educational Qualifications:

- Ph.D (Electrical & Electronics Engineering) from JNT University, Hyderabad, 2009.
- M.Tech (Computer Science Engineering) from JNTU College of Engineering, Kakinada in 2004 with First Class.
- M.E (Electrical Power Engineering) from Maharaja Sayajirao University of Baroda, Baroda in 1999 with First Class.
- B.E (Electrical & Electronics Engineering) from Vijaynagar Engineering College in 1996 with First Class with Distinction.
- Intermediate from VV&M Junior College, Ongole with First Class in 1988.
- SSC in St. Theresa's High School, Ongole with First Class in 1986.

Professional Experience:

- Working as Professor in Department of Electrical and Electronics Engineering Department, PVP Siddhartha Institute of Technology, Vijayawada from 31st August 2015 to till date
- Worked as Professor in Department of Electrical and Electronics Engineering Department, K L University, Vijayawada from 18th January 2010 to 30th August 2015.
- Worked as Professor in Department of Electrical and Electronics Engineering Department, QIS Institute of Technology, Ongole from 27th June 2009 to 17th January, 2010.
- Worked as Professor in Department of Electrical and Electronics Engineering, QIS College of Engineering and Technology, Ongole from 01st March 2008 to 26th June 2009.
- Worked as an Associate Professor in Department of Electrical and Electronics Engineering, QIS College of Engineering and Technology, Ongole from 01st March 2004 to 29th February 2008.
- Worked as an Assistant Professor in Department of Electrical and Electronics Engineering, QIS College of Engineering and Technology, Ongole from 31st August 2000 to 29th February 2004.
- Worked as Lecturer in Department of Electrical and Electronics Engineering, SSN Engineering College, Ongole from 27th December 1999 to 30st August 2000.
- Worked as Research scholar for a period of six months at Power System Cell, Electrical Research Development Association (ERDA), Baroda under the Guidance of Dr.M.Ramamoorthy.

Administrative Experience:

- Working as Head, Department of Electrical and Electronics Engineering, PVP Siddhartha Institute of Technology, Vijayawada from August 2015 to till date.
- Worked as Head, Department of Electrical and Electronics Engineering, KL University, Vijayawada from July 2010 to August 2015.
- Worked as Head, Department of Electrical and Electronics Engineering, QIS Institute of Technology, Ongole from June 2009 to January 2010.
- Worked as In-charge Head, Department of Electronics and Communication Engineering, QIS College of Engineering & Technology, Ongole from June 2008 to December 2008.

- Worked as JKC Coordinator in QIS College of Engineering & Technology, Ongole from August 2005 to August 2007.
- Worked as Placement Coordinator in QIS College of Engineering & Technology, Ongole from August 2005 to August 2007.
- Worked as Head, Department of Computer Science & Engineering in QIS College of Engineering & Technology, Ongole from March 2003 to 1st September 2004.
- Worked as Head, Department of Electrical and Electronics Engineering, in QIS College of Engineering & Technology, Ongole from August 2000 to July 2001.

Other Information:

- Ratified as Professor in Electrical & Electronics Engineering by JNTUK, Kakinada in March, 2018.
- Ratified as Professor in Electrical & Electronics Engineering by K L University in March, 2011.
- Ratified as Associate Professor in Electrical & Electronics Engineering by JNTU, Hyderabad in August, 2005.
- Ratified as Assistant Professor in Electrical & Electronics Engineering by JNTU, Hyderabad in November, 2000.

Recognitions:

- Fellow of Institute of Engineers (India) (No.F-116463-1).
- Senior Member in IEEE Professional Society (No.91167136).
- Life Member in Solar Energy Society of India (No.LM/1717/2011).
- Life Member in ISTE Professional Society (No. LM 79802)
- Member in International Association of Engineers (No.11738).
- Counselor for IEEE Student Branch of KL University from 2012 to 2015
- Chairman of “Board of Studies” Electrical & Electronics Engineering, KL University, India from February 2010 to June 2015.
- Chairman of “Board of Studies” Electrical & Electronics Engineering, Koneru Lakshmaiah College of Engineering (Autonomous), India from February 2010 to March 2012.
- Member in Board of Studies of Narasaraopet Engineering College, Narasaraopet from August 2015 to till date.
- Member in Board of Studies of Vishnu Institute of Technology, Bhimavaram for the academic Year 2019-20.
- Member in Doctoral Review Committee in Vignana’s University Guntur and VIT University, Vellore.
- Member in Academic Council of KL University, India from February 2010 to August 2015
- Member in Research Board of KL University, India for the academic year 2012-13 to 2013-14
- Recognized Supervisor in Acharya Nagarjuna University and JNTU, Kakinada.
- Best Teacher Award for the academic year 2010-11, 2011-2012, 2012-13 and 2013-14 at KL University.
- Member in Editorial Board of International Journal of Advances in Soft Computing Technology.
- Member of the Editorial Board of i-manager’s journal on power systems Engineering (JPS) from 2012 to till date
- Member in Malpractice Committee in PVP Siddhartha Institute of Technology, Vijayawada from September 2015 to till date.
- Reviewer to the Technical Papers of Institute of Engineers’, Elsevier etc.,
- Chair for Technical Session on PEPE: Motors & Drives in IEEE International Conference TENCON 2016 held at Marina Bay Sands, Singapore on 25th November, 2016.

- Member in Member Development Committee in IEEE Vizay Bay Sub Section for the Year 2018
- Chair for Technical Session on PEPE : Motors & Drives in IEEE International Conference TENCON 2017 held at Penang, Malaysia on 7th & 8th November, 2017

Publications

International Journals	: 39
National Journals	: 02
International Conferences	: 27
National Conferences	: 02

Sponsored Projects:

Received Rs. 13 Lakhs Grant in Aid under Modernization and Removal of Obsolescence Scheme (MODROB) from All India Council for Technical Education for the Year 2017-18

Research Guidance:

- Guided Twelve students at Post Graduate level
- Guided Three Ph.D Scholars
- Guiding Five Ph.D Scholars as Supervisor and Two Ph.D scholars as Co-supervisor

Books Published:

- 1) Design of Efficient low voltage high current DC-DC Power Supplies Design & Applications, LAMBERT Academic Publishing, ISBN: 978-3-8484-3092-5
- 2) Compensation of voltage Sag and Swells by Custom Power Devices Essentials & Applications, LAMBERT Academic Publishing, ISBN: 978-3-8473-1809-5
- 3) ZVT Based Resonant Converter Analysis & Applications, LAMBERT Academic Publishing, ISBN: 978-3-8484-8523-9

(Dr.M Venu Gopala Rao)