



SIDDHARTHA INSTITUTE OF TECHNOLOGY

(AUTONOMOUS)

KANURU, VIJAYAWADA - 520 007.

(Sponsors : Siddhartha Academy of General & Technical Education, Vijayawada)

Affiliated to JNTUK, Kakinada

Approved by AICTE, Accredited by **NAAC** with '**A+**' Grade
An ISO 9001 : 2015 Certified Institution

Ref :

Date : 05.12.2019.

PVPSIT-ECE DEPT BOARD OF STUDIES (BOS) MEETING INVITATION

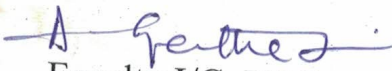
Greetings. We are cordially inviting you for the **14th BOS Meeting** of ECE Department. The BOS meeting will take place on Saturday 07th December, 2019 in e-class room, Dept. of ECE, PVPSIT at 02.00 P.M.

The agenda of the BOS Meeting is as follows:

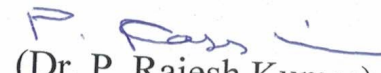
1. Syllabus for proposed courses of II B.Tech. Electronics & Communication Engineering Program in PVP-19 Regulation
2. To discuss Course Outcomes (COs) for the above courses
3. Any other item as per requirement of ECE Department

We look forward to meeting you in PVPSIT, Vijayawada.

Thanking you,


Faculty I/C, BOS

CC to Principal, PVPSIT


(Dr. P. Rajesh Kumar)

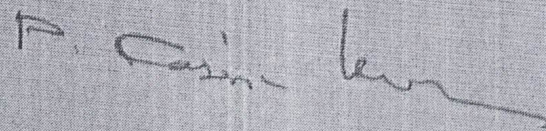
Prof. & HOD., Dept. of ECE

Dr. P. RAJESH KUMAR
Ph.D (IIT-M)

Head & Professor
Dept. of Electronics & Communication Engg.
PVP Siddhartha Institute of Technology
KANURU, VIJAYAWADA-520 007.

14th BOS Meeting Minutes- 07-12-2019

1. It is advised to show only course credits & LTP in the course structure.
2. Before the course syllabus the CO-PO articulation table should present in the Handbook/Syllabus Book.
3. It is advised to swap Signals & systems (II year II Sem) and Digital System Design(II year I Sem)
4. Analog and Digital Communication Course has to be divided in to two courses titled as a) Analog Communications and b) Digital Communications. The Digital Communication course has to be accommodated by removing AI tools lab, Life Sciences for Engineers lab and Product Innovation and Design Thinking Lab courses
5. It has been suggested to give detailed syllabus for the following subjects:
 - (i) Network Theory and Analysis
 - (ii) Probability Theory and Stochastic Process
 - (iii) Digital Logic Design
6. Course Outcomes of the II year subjects should be revised as per the given suggestions.
7. It is suggested to prepare AI Tools syllabus in consultation with CSE faculty and IT Industry people to incorporate more tools relevant to Electronics & Communication Engineering
8. It is suggested to rename Complex Variables & Transform Techniques as Engineering Mathematics – III and include Numerical Methods as per the GATE syllabus.



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