

BIO DATA

Name : **Komma Anitha**

Fathers Name : K.V.Bhaskara Rao

Qualification : M.E (Communication Systems)

Total experience : 24 years.

a) 2 yrs 8 months as Lecturer

b) 2 yrs as senior software engineer

c) 19 yrs 6 months as Associate Professor

Address for communication : Komma Anitha

Flat No: 208, A1 Block, Venkatram Grand

Ramapuram Colony Road, Poranki,

Krishna Dt - 521137.

Mobile: 9490647666

E-mail address : anithakomma108@gmail.com

Professional Societies : Life member ISTE (LM53242)

Life member ISSE-LM03636

Member IETE (M234490)

Educational Qualifications :

Degree	Year Of Passing	University/ College	Specialization	Percentage	Remarks
M.E	January 1995	P S G College of Technology, Coimbatore	Communication Systems	82 %	Stood first in the class
B.Tech	June 1992	Nagarjuna Univesity, V R Siddhartha Engineering College, Vijayawada	Electronics & Communications Engineering	80 %	Stood first in the class
D.E.C.E	June 1988	State Board of Technical Education, KES Polytechnic, VJjayawada	Diploma in Electronics and Communications Engineering	84 %	Stood first in the class

Details of Experience:

Organization	Designation	Period
PVP Siddhartha Institute of Technology, Kanuru, Vijayawada	Associate Professor	May 2012 to till date
Sir C R R College of Engineering, Eluru	Associate Professor	Feb 2000 to Apr 2012
CBSI Pvt.Ltd, Chennai	Senior Application Developer	Feb 1998 to Feb 2000
V R Siddhartha Engineering College, Vijayawada	Lecturer	Jan 1995 to Sept 1997

Responsibilities undertaken in the College & Department:

- PMKVY(Pradhan Mantri Kaushal Vikas Yojana) Nodal Officer
- Member in Academic Council
- Incharge of Dept. Examcell.
- Co-ordinator for III B.Tech, ECE, Sec – I & II
- Co-ordinator for II B.Tech, ECE, Sec – I & II
- BOS member
- Department Co-ordinator for NBA Criterion – III
- Department Co-ordinator for NBA Criterion – V
- Department Co-ordinator for NAAC Criterion – I
- Department Development Council member.

Subjects Handled:

- Digital Signal Processing
- Switching Theory and Logic Design
- Signals and Systems
- Digital IC Applications
- Digital Electronics
- VHDL Programming
- Linear Integrated circuits
- Pulse and Digital Circuits
- Analog Electronic Circuits

Courses designed with detailed syllabi for B.Tech (PVP12 & PVP14)

- Signals and Systems
- Switching Theory and Logic Design
- Digital Signal Processing
- Basic Simulation Lab

Certification Courses completed:

- Top 1% of certified candidates in 12 weeks NPTEL online certification course on “Principles of Signals and Systems” and obtained 99% with Elite + Gold certificate.
- Top 1% of certified candidates in 12 weeks NPTEL online certification course on “Digital Electronic Circuits” and obtained 82% with Elite + Silver certificate

Journal publications:

- K.Anitha, M.Srinivasa Rao “*Discrete Extended Kalman Filter Based Ultrasonic Time-Of-Flight Estimation*” International Journal of Engineering Research and Application, Vol 8, Issue 11(part-I), pp. 31-37, November 2018, ISSN 2248-9622 (UGC Approved Journal).
- K.Anitha, M.Srinivasa Rao “*FPGA Based Controller Design for Mobile Robots*” International Research Journal of Engineering and Technology, Vol 5, Issue 10, pp. 1494-1498, October 2018, ISSN 2395-0056.
- M.Srinivasa Rao, K.Anitha “*Improving the Uplink Achievable Rate and Power Allocation in LTE-Advanced Networks*” International Journal of Engineering and Science Research, Vol 8, Issue 10, pp. 36-42, October 2018, ISSN 2277-2685 (UGC Approved Journal).
- M.Srinivasa Rao, K.Anitha “*Improved Performance of Turbo Codes using Iterative Decoding with Additional Convolutional Interleaver*” Journal of Emerging Technologies and Innovative Research, Vol 5, Issue 10, pp. 373 - 378, October 2018, ISSN 2349-5162 (UGC Approved Journal).
- K.Anitha, P.Chandra Sahaja “*Scheduling Mechanisms for Wireless Networks using Coarse-Grained Back Pressure Control*” International Journal on Electronics & Wireless Communication Engineering, Vol 9, Issue 9, November 2017, ISSN 2454-9924, pp. 1-7.
- K.Anitha, Gayathri B “*A Novel Very Low-Complexity Asymmetrical Relay Selection and Association for Multi-User Multi-Relay MIMO Uplink*” International Journal for Modern Trends in Science and Technology, Vol 2, Special Issue 01, October 2016, pp. 150-156.

- G.Revathi, K.Anitha “*Real time Digital Signal Processing Based on Optical OFDM*” International Journal of Research in Computer and Communication Technology, Vol 4, Issue 10, October 2015, pp. 879-884.
- V.Padma, K.Anitha “*Low Complexity Range Measurement System*” International Journal of Scientific Research and Education, Vol 2 Issue 11, Nov 2014, pp. 2362-2370.
- V.Padma, K.Anitha “*High Accuracy Range Measurement System*” International Journal of Engineering & Science Research, Vol 4 Issue 9, Sept 2014, pp. 504-509.
- K.Anitha, V.Vamsi Sudheera, Dr P.Rajesh Kumar, M.Narendra Kumar, “*High Accuracy Time-of-Flight Measurement Using DSP Techniques for Mobile Robots*” International Journal of Electronics & Communication Technology, Vol 5 Issue SP-3, Jan-Mar 2014, pp. 45-49.
- K.Anitha, Sk Mastan Vali, Dr P.Rajesh Kumar, “*Novel Approach of Stationary & Non Stationary Implementation of NLMS & RLMS Algorithms for Suppression of Noise in Cardiac Signals*”, International Journal of Engineering Research and Applications, Vol. 3, Issue 6, Nov-Dec 2013, pp.1022-1025.
- V.Vamsi Sudheera, K. Anitha, Dr P.Rajesh Kumar, K.Soujanya, “*Development of Hardware and Software for Obstacle Detection and Distance Estimation in Mobile Robots*”, International Journal of Computer Science information and Engineering Technologies, Issue 2 Vol 1 Series 4, April 2012.
- K. Anitha, Dr P.Rajesh Kumar, V.Vamsi Sudheera, M.Narendra Kumar, “*Time-of-Flight Measurement For Ultrasonic Sensors using Digital Signal Processing Techniques*”, International Journal of Electronics & Communication Technology, Vol 2, SP1, Dec 2011, pp. 267-270.
- K. Srilakshmi, K. Anitha, Dr P.Rajesh Kumar, G.R.L.V.N Raju, “*Design and Implementation of Distance measuring Digital Hardware*”, International Journal of Electronics & Communication Technology, Vol 2, SP1, Dec 2011, pp. 222-225.
- K.Anitha, M.Srinivasa Rao, CRS Murthy, “*Embedded Bluetooth data acquisition system based on ARM for unmanned Underwater vehicle(UUV)*”, International journal of Computer science and network security, Vol 11, No.7, pp. 136-140 July 2011.
- Prof M.Srinivasa Rao, Dr P.Rajesh kumar, K.Anitha, Addanki Srinu, “*Modified maximum A posteriori algorithm for iterative decoding of Turbo codes*”, International Journal of Engineering Science and Technology (ISSN 0975-5462), Vol 3, No. 8, pp. 6609-6615 Aug 2011.
- CRS Murthy, G.Srilatha, K.Anitha, Ch.Ravi Kumar, M.Srinivasa Rao, “*Counter Jamming method based on Chaos for secure communication in multiuser Environment*”, International journal of Electronics and Communication Technology, Vol 2, Issue 3, pp. 199-203, Sept 2011.

International Conferences:

- K. Anitha, V.Vamsi Sudheera “*A Robust Ultrasonic Distance Measurement System with Discrete Extended Kalman Filter*”, Proceedings of the

International Conference on Electrical, Electronics, Signals, Communication and Optimization (EESCO) 24th & 25th Jan 2015.

- Kamma Srinivasa Rao, Kantamneni Kranthi, K. Anitha, Dr P.Rajesh Kumar “*Serpentine – A rescue Snake Robot*”, 7th IEEE International Conference on Industrial and Information systems (ICIIS) 6th - 9th Aug 2012.
- K. Anitha, M.Srinivasa Rao, Rangarao Orugu, “*Quality of Transmission Aware Routing in All-Optical WDM Networks*”, Proceedings of Joint International Conference on Swarm, Evolutionary & Memetic Computing (SEMCCO) and Fuzzy & Neural Computing Conference (FANCCO) 19th – 21st Dec 2011.
- K. Anitha, Dr P.Rajesh Kumar, V.Vamsi Sudheera, M.Narendra Kumar, “*Time-of-Flight Measurement For Ultrasonic Sensors using Digital Signal Processing Techniques*”, Proceedings of the International Conference on Advances in Computer Science, Communication & Bio-Instrumentation Engg (ICACCBIE-11) 8th & 9th Dec 2011.
- K. Srilakshmi, K. Anitha, Dr P. Rajesh Kumar, G.R.L.V.N Raju, “*Design and Implementation of Distance measuring Digital Hardware*”, Proceedings of the International Conference on Advances in Computer Science, Communication & Bio-Instrumentation Engg. (ICACCBIE-11) 8th & 9th Dec 2011.

National Conferences:

- K.Anitha, B.Susmitha, M.Srinivasa Rao “Eigenface Recognition Using PCA”, at First Annual Conference on Computer Aided Developments in Electronics and Communication(CADEC-2019) by VIT-AP University, Amaravathi on 2nd & 3rd March 2019.
- K.Anitha, Gayathri B “*A Novel Very Low-Complexity Asymmetrical Relay Selection and Association for Multi-User Multi-Relay MIMO Uplink*” Proceedings of National Conference in Computing, Electrical, Electronics and Sustainable Energy Systems (NCCESES-2016) 6th & 7th October 2016.
- K.Anitha, V.Vamsi Sudheera, Dr P.Rajesh Kumar, M.Narendra Kumar, “*High Accuracy Time-of-Flight Measurement Using DSP Techniques for Mobile Robots*” National Conference on Research Issues and Research trends in Electronics and Communication Engineering (NCRRECE-2014) 24th & 25th Jan 2014.
- K. Anitha, D.V.R Chaitanya, I.Jayaram Kumar, Dr P. Rajesh Kumar “*Design of FPGA Based Controller for Mobile Robots*”, Proceedings of the IEEE technically co-sponsored National Conference on Advances in Communication, Navigation & Computer Networks (ACNCN-2012) 17th & 18th Mar 2012.
- K. Anitha, V. Vamsi Sudheera, M. Narendra Kumar, Dr P. Rajesh Kumar “*Development of Hardware and Software for Obstacle Detection and Distance Estimation in Mobile Robots*”, Proceedings of the IEEE technically co-sponsored National Conference on Advances in Communication, Navigation & Computer Networks (ACNCN-2012) 17th & 18th Mar 2012.

- K.Anitha, V.Vamsi Sudheera, M.Narendra Kumar, “*Digital Time-of-flight Measurement for Ultrasonic Sensor using Cross Correlation Estimation*” Proceedings of National Seminar on Role of Radars in Atmospheric and Ionospheric Studies (RAISE’12) 4th & 5th Jan 2012.

Workshops/Training Programs Attended:

- Attended a five-day workshop on “Cryptography and Coding Theory” at NIT Warangal from 28th January to 1st February 2019.
- Attended a one-day workshop on “Digital Health Interventions” organized by Department of Medical Education at Dr Pinnamaneni Siddhartha Institute of Medical Sciences and Research Foundation, Chinna Avutapalli on 22/11/2018.
- Attended a Seminar on “Engineering Education & Research “ conducted by National Instruments at Fortune Murali Park, Vijayawada on 29/06/2019
- Attended a three-day training program on “Signal Processing with Simulink” organized by Mathworks at PVPSIT from 13th -15th December 2017.
- Participated in one-week Faculty Development program on “New Trends and Methodologies in Engineering Education” organized by E&ICT Academy, NIT Warangal at PVPSIT from 24th – 29th April 2017.
- Attended a five day workshop on “Advanced Digital Signal Processing & its Applications” at VR Siddhartha Engineering College, Vijayawada.
- Attended a two day workshop on “Recent Research Trends in Next Generation Wireless Technologies” at College of Engineering, Guindy, Anna University, Chennai.
- Attended a two day training program on “Mentor Graphics Backend EDA tools” by Trident tech labs at PVPSIT
- Organized a two day traing program on “Mentor Graphics tools” by Trident tech labs.
- Attended a seminar on “Recent Research Trends in Microwave Engg.” By Dr P.V.D.Soma Sekhara Rao, Prpfessor, JNTUH at PVPSIT.
- Attended a one week Faculty Development Program on “Recent Trends in Systems and Signal Processing” at LBRCE, Mylavaram.
- Attended a one week faculty development program on “Signals and Systems” conducted by IIT, Kharagpur at PVPSIT.
- Attended a five day faculty development program at VRSEC, Vijayawada

- Attended two day workshop on “DSP Architectures and Applications” at Bhoj Reddy Engg. College for Women, Hyderabad.
- Attended two day workshop on “Challenges in VLSI design” at RVR & JC College of Engineering, Guntur.
- Attended two day workshop on “VLSI design” at Gudlavalleru Engineering college, Gudlavalleru.
- Attended a five-day training program on Instructional design and delivery at Sir CRR College of Engg., Eluru.
- Conducted two day workshop on FPGA based system design at Sir CRR College of engg., Eluru.
- Attended a two day workshop on “Digital Design Simplified” at Sri Vishnu Engg. College for women, Bhimavaram.
- Attended a two day workshop on “Embedded design using Xilinx FPGA’s” at V R Siddhartha Engg. College, Vijayawada.
