

## CURRICULUM VITAE

L.Madhavi Devi,  
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### CAREER OBJECTIVE

To work with an organization in the most challenging environment to contribute my best efforts for the achievement of organizational goals

### EDUCATIONAL PROFILE

Course	Institution	Board / University	Year of Completion	Aggregate (%)
PhD (E.C.E)	Carrier Point University, Rajasthan.	Carrier Point University	2019	NA
M. Tech (E.C.E)	VR Siddhartha college of Engineering, Vijayawada.	Acharya Nagarjuna University	2009	65
M.Sc (Electronics)	Sri.Y.N.College,Narsapur.	Andhra University	2000	63
B.Sc (Electronics)	St.Theresa's Autonomous College,Eluru.	Andhra University	1998	74
Intermediate (M.P.C)	Siddhartha Residential College, Eluru.	Board of Intermediate Education, AP.	1995	74
SSC	Z.P.P High School, Veeravalli,Krishna dt.	Board of Secondary Education, AP.	1994	75

### PROFESSIONAL BODY MEMBERSHIPS

Life member, ISTE - LM102527

Member, ISSE - LM03639

### **WORK EXPERIENCE**

S. NO	Position	Institute	Period	Experience
1	Assistance Professor, E.C.E	PVP Siddhartha Institute of Technology, Vijayawada.	07-10-2009 onwards	Till date

### **SHORT TERM COURSES/WORKSHOPS/CONFERENCES ATTENDED/ORGANIZED**

#### **Workshops:**

- A one day workshop on “Research Methodologies in Engineering” held on 23<sup>rd</sup> oct,2010 conducted by dept of I.T, at PVPSIT.
- A 2 day workshop on “Lab View software” organized by ECE dept in association with M/S Cranes software International Ltd, Bangalore during 27<sup>th</sup> and 28<sup>th</sup> Oct, 2010.
- A 2 day workshop on “VLSI Deign” organized by ECE dept, VRSEC during 9<sup>th</sup> & 10<sup>th</sup> may2011
- A 2 day workshop on “Embedded design using Xilinx FPGA’s” organized by TIFAC-CORE in telematics and ECE dept, VRSEC during 8<sup>th</sup> & 9<sup>th</sup> July 2011.
- A Two day workshop on “Lab view and its engineering applications” during 14th & 15th Sept. 2012 at PVPSIT.
- A 4 day workshop on “Research Methodology” from 7<sup>th</sup> to 10<sup>th</sup> February, 2014 conducted by KLUUniversity, Guntur.
- Attended a Two Day National level Workshop on “Signal Processing and Wireless Communications” organized by Dept.of ECE, Bapatla Enginee ering College, Bapatla, from 25th June, 2014 to 26th July, 2014
- A webinar on “Sensor Measurement Fundamentals” webcasted by National Instruments, India from 28th January, 2014 to 31st January, 2014.
- A seminar on “Intellectual Properties awareness & its current scenario” at KLUUniversity on 8th march, 2014.
- Attended a guest lecture on “Managing Bharat to make Prosperous, Powerful, Intellectual and Ethical Resource Person” organized by Dept.of MBA, PVPSIT on 13th July, 2015.
- Attended a Guest lecture on “Outcome Based Education” at PVPSIT on 28th, September, 2015.
- Attended “A mind Training Programme” organized by International Youth Fellowship of South Coreans at PVPSIT, on 18th December, 2015.
- Attended a seminar on “Use of Bloom’s Taxonomy in Teaching, Learning & Assessment” on 6th August, 2016 in R & D Center., Dept. of ECE, PVPSIT. Resource Person: Dr. K. Ramanjaneyulu, Professor
- Attended a five day workshop on “IOT Applications” conducted by department IT PVSIT in association with NIT Warangal.
- Attended a One week on-line workshop on “ISTE STTP CMOS mixed signal and Radio Frequency” from 30th January, 2017 to 4th February, 2017 organised by IIT, Kharagpur at PVPSIT.

- Attended Three day workshop on “Scientific Engineering Educational Practices“ at VRSEC, IQAC from 27th January, 2017 to 29th January, 2017.
- Attended seminar on IPR7 Patents on 29th September, 2018 conducted by ISTE faculty chapter in association with IQAC. Resource Person: Mr. K. Ravindra Nath, Manager, SUTech Patent art services Pvt. Ltd., Hyderabad
- attended two day workshop on “Advances in Electronics and Communication Engineering (AECE – 2017)” on 26th& 27th August, 2017 at JNTUK, Kakinada
- Attended one day workshop on Digital Health Interventions organized by Dept. of Medical education at Dr. Pinnamaneni Siddhartha Medical Science and Research Foundation, Chinautupally on 22nd December, 2018.
- Attended a one day workshop on Basic Life Support organized by Anesthesia Dept. of Dr. P.S.I.M.S. Chinautupally on 27th February, 2019.
- Attended seminar on IPR7 Patents on 29th September, 2018 conducted by ISTE faculty chapter in association with IQAC.
- Attended a two day hands on workshop on PCB Design and Prototype development organized by the department of Electronics and Communication Engineering, Vellore Institute of Technology, A.P. during 8th – 9<sup>th</sup> December, 2018.
- Attended one day Faculty Development Program on Intellectual Property Rights & Innovation organized by Dept. of MBA in collaboration with IQAC cell on 29th April, 2019.

### **1WEEK FDP:**

- A one week course work on “Lab View software” organized by TIFAC-CORE in telematics and ECE dept, VRSEC during 6<sup>th</sup> to 11<sup>th</sup> June 2011.
- A one week training programme on “Instructional Design and Delivery systems” conducted by NITTTR, Chennai at PVPSIT, from 8<sup>th</sup> to 12<sup>th</sup> July 2013.
- A one week FDP on “Advanced Digital Signal Processing & Applications (ADSP-A)”at VRSEC from 7<sup>th</sup> to 13<sup>th</sup> September 2015.
- Attended a five day workshop on “IoT Applications” conducted by department IT PVSIT in association with NIT Warangal.
- Attended “Art of Living” an Awareness Program conducted on 4th August, 2017 and Training Session was conducted from 18th August, 2017 to 20th August, 2017.
- A One week on-line workshop was conducted on “ISTE STTP CMOS mixed signal and Radio Frequency” from 30th January, 2017 to 4th February, 2017 organised by IIT, Kharagpur at PVPSIT.
- A five day workshop was conducted on “Lab view Core-I & II” from 27th January, 2017 to 31st, January, 2017.
- Attended Five day Training program on “NI MYDAQ” from 5th June, 2017 to 9th June, 2017
- Attended one week ICT based Faculty Development Program on VLSI Design conducted by NITTTR Chandigarh, organized by dept. of ECE during 24th – 28th September, 2018 at PVPSIT knowledge Centre.
- Attended a one week Faculty Development Program on Recent Advances in Communications and Signal Processing organized by E&ICT Academy, NIT Warangal and NRI Institute of Technology during 29th October, 2018 - 4 th November, 2018.
- Attended a five day Faculty Development Program on NBA Accreditation conducted by NITTTR at PVPSIT during 22nd -26th April, 2019.
- Attended one week Faculty Development Program on Signal Processing - Deep Learning organized by Vijaya Institute of Technology, Enekepadu in association with E&ICT Academy, NITW during 10th – 15th June, 2019.

### **2 WEEKS FDP:**

- A two week ISTE workshop on “Solar photovoltaics” conducted by IIT Bombay at PVPSIT, from 12<sup>th</sup> to 22<sup>n</sup> December 2011.
- A two week ISTE workshop on “SIGNALS & SYSTEMS” from 2<sup>nd</sup> January, 2014 to 12<sup>th</sup> January, 2014, conducted by National Mission on Education through ITC (NMEICT) through synchronous and Asynchronous Instruction.
- Attended a one week FDP on “CMOS Integrated Circuits for Instrumentation and IOT Applications” conducted by VRSEC in association with NIT Warangal from 22<sup>nd</sup> to 28<sup>th</sup> January, 2018.
- Attended a one week ICT based Faculty Development Program on VLSI Design conducted by NITTTR Chandigarh, organized by dept. of ECE during 24<sup>th</sup> – 28<sup>th</sup> September, 2018 at PVPSIT knowledge Centre.
- Attended a summer training programme on Introduction to Programming the Internet of Things (IOT) organized by Dept. of ECE, PVPSIT in association with by APSSDC & “COURSE-ERA” (University of California, IRVINE) from 24<sup>th</sup> April – 23<sup>rd</sup> May, 2019.Th

### **PAPERS PUBLISHED**

1. EEG Signal Processing for Abnormality Detection using DWT, SE and KNN Techniques - JETIR, Vol:5; Issue:9; Pages:88-99; December – 2018.
2. Electronic smart shopping trolley in super market - Journal of Applied science and Computations, Volume 6, Issue 3, March – 2019.
3. Unmanned Ground Vehicle - Journal of Applied science and Computations, Volume 6, Issue 4, April – 2019

### **CONFERENCES**

1. Electronic smart shopping trolley in super market - Journal of Applied science and Computations, Volume 6, Issue 3, March – 2019.

### **SUBJECTS HANDELED**

- 1) Electronic Devices And Circuits
- 2) Electronic Circuit Analysis
- 3) Analog Electronic Circuits
- 4) Analog Communications
- 5) Electrical Electronics Engineering
- 6) Switching Theory Logic Circuits
- 7) Digital Communications
- 8) Digital Signal Processing
- 9) Optical Fiber Communications

10) Cellular and Mobile Communications

11) TV Engineering

12) GPS

**LABS HANDLED**

1) Electronic Devices And Circuits

2) Electronic Circuit & Pulse & Digital Circuits

3) Basic Electronics Engineering

4) Microprocessors & Interfacing

5) Analog Communications

6) Digital Communications

7) LDIC

8) Pulse and Digital Circuits.

9) Analog Electronic Circuits

10) Digital Signal Processing Lab

11) Basic Simulation

12) Multisim

## **PERSONAL PROFILE**

- Name : L.Madhavi Devi
- Date of Birth : 30/04/1976
- Father's name : Babu Venkata rao
- Languages Known : Telugu, Hindi & English
- Permanent Address : Flat no:403, Gayatri Residency,  
MVR 2<sup>nd</sup> Block  
Mythri Nagar,  
Kanuru,  
Vijayawada. Pin Code: 520001
- Contact No : 9912454374,9848352348
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## **DECLARATION**

I hereby declare that the information furnished above is true to the best of my knowledge.

**L.Madhavi Devi**