

Prasad V Potluri Siddhartha Institute of Technology
Department of Electronics & Communication Engineering

A.Y:2025-26

Workshops Organized for Students

S. No.	Title	Duration	Resource Persons	Participants
1	A 5 Days Workshop on VLSI Tools & FPGA: Design to Implementation	16.09.2025to 18.9.2025, 20.9.2025 & 22 .09.2025	Dr. Ch. Gangadhar Dr.T. Mahalakshmi Mrs. Praveena Bhai	III ECE & IVECE
2	Getting started with SAM E54 Embedded development - From sensor data to serial output 26.09.2025	26.09.2026	Mr. V. Sarath, Lead Engineer, Microchip, Hyderabad	III ECE students
3	One-week workshop on PCB Design II ECE_ SI II ECE_ SII (6 days)	06.10.2025 to 11.10.2025 & 13.10.2025 to 18.10.2025	Rubeena ,Trainer APSSDC	II ECE students
4	AI in Electronics	02.03.2026 to 07.03.2026	Maganti Srivalli Trainer Jaishna Software	II ECE students

Faculty Development Programs

S. No.	Title	Duration	Resource Persons	Participants (Internal +External)
5	Intelligent Antenna Systems and Electromagnetic Techniques Leveraging AI for RF and Microwave Technologies in IoT and Biomedical Applications	01.12.2025 to 05.12.2025	Dr.Satya deep Das, RF Design & Life Cycle specialist , Rosen Berger, Bangalore , Sri. Akula Naresh , Technical Director (AI & Drones), DRDO. Hyd , Dr. Debojith De, Sr. Engineer, Mercedes Benz Research, Bangalore , Sri. Deba prasad Barad , Manager, R&D Engg, Astra Microwaves, Hyd , Mr. Dharmendra Naik, Manager, Business Development, MMRFIC Pvt. Ltd., Hyd	33
6	Applications of Lab View in Engineering	01.07.2025 to 05.07.2025	Dr. A. Geetha Devi	8
7	A one day workshop on Transforming the future of Research with IEEE	20.11.2025	Mr Shaji John, Mr Collin DMello VP of Sales, South Asia, EBSCO Information Services International Area Manager, IEEE	41
8	VLSI Design & Methodologies	22.06.2026 to	1. Dr. B K N Srinivasa Rao, NIT, Warangal.	27

		26.06.2026	2. Dr. B. Naresh Kumar Reddy, NIT, Trichy 3. Dr. Krishna Chaitanya A NIT, A.P. 4. P. Manikanta, INTEL corporation, Bangalore. 5. Ch.Surya, Sense Semiconductor & IT Solutions Pvt Ltd, Mangalagiri	
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Conferences

S. No.	Title	Duration
9	Automotive & EV Conference 2025 in association with Siddhartha Academy of Higher Education, Efftronics Pvt., Ltd., & INCOSE, India	31.10.2025 to 01.11.2025
10	International Conference on Intelligent Electronics and Communication Systems – 2025 (ICIECS-2025) in association with The Institution of Electronics and Telecommunication Engineers (IETE) Vijayawada Centre	27.12.2025

(Dr.C.Subba Rao)
Professor & HoD



Prasad V. Potluri Siddhartha Institute of Technology
(Accredited by NAAC: A+)

Department of Electronics & Communication Engineering
(Accredited by NBA)

Approved by AICTE New Delhi and Permanently Affiliated to JNTUK, Kakinada

A 5 Days Workshop on VLSI Tools & FPGA: Design to Implementation

**Resource Persons :- Dr. Ch. Gangadhar
Dr. T. Mahalakshmi
Mrs. Praveena Bai**

No Registration Fee

- **Dates of Workshop:** 16 September 2025 - 18 September 2025, 20 & 22 September 2025
- **Venue:- EMBEDDED SYSTEMS LAB**
- **Theme:-** Hands-on training on Cadence VLSI Tools & Nexys Artix-7 FPGA evaluation boards to learn about digital design and FPGA programming.
- **Interested Students Send Their Details to this Email Id:**
dasari.maha@pvpsiddhartha.ac.in

For any Queries Contact

Dr. Ch. Gangadhar

Cell: +91-9440577560

PRASAD V. POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY
(AUTONOMOUS), KANURU, VIJAYAWADA-7
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 24.09.2025

This is to inform all the concerned that workshop on "Getting Started with SAM E54 Embedded Development - From sensor data to serial output — your first steps in embedded design.", by alumni is being arranged by ECE Dept. with the details as under:

Topic: "Getting Started with SAM E54 Embedded Development"

Resource person: Mr. V. Sarath
Lead Engineer, Microchip,
Hyderabad. (Alumni ECE 2012-2015)



Venue: DSP Lab
Time & Date: 9.00 A.M , 26-09-2025

Note: 1) Interested students of III B.Tech. ECE are required to give their names to the class CR.


Alumni Co-ordinator


(Dr. C. Subba Rao)
PROF. & H.O.D.

24/9/25

CC: 1. Notice Board.

PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY

KANURU, VIJAYAWADA – 7

DEPARTMENT OF ECE.

Circular

Dt. 26.09.2025

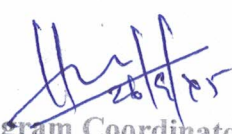
This is to inform that the Department of Electronics & Communication Engineering in association with Andhra Pradesh State Skill Development Corporation is conducting a 6 day workshop on “**PCB Design Program**” for students of II B.Tech ECE from 06.10.2025 to 11.10.2025 (for Sec 1) & 13.10.2025 to 18.10.2025 (for Sec 2).

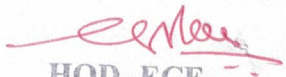
Resource Person:

Rubeena, Trainer APSSDC

Venue:

Embedded Systems Lab (Room No.357) & EDC Lab (Room No.354)


Program Coordinator:
Dr. Haji Habibulla MD


HOD -ECE
Dr. C Subba Rao

PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY

KANURU, VIJAYAWADA – 7

DEPARTMENT OF ECE.

Circular

Dt. 27.02.2026

This is to inform that the Department of Electronics & Communication Engineering in association with Andhra Pradesh State Skill Development Corporation is conducting a 6 day workshop on “**AI in Electronics**” for students of II B.Tech ECE (S1 & S2) from 02.03.2026 to 07.03.2026

Resource Person:

Maganti Srivalli, Trainer
Jaishna Software

Venue:

Embedded Systems Lab (Room No.357)


Program Coordinator:

Dr. Haji Habibulla MD


HOD -ECE

Dr. C Subba Rao

27/02/26

REGISTRATION FORM

Five Day Faculty Development Program on

**“Intelligent Antenna Systems and Electromagnetic
Techniques Leveraging AI for RF and Microwave Technologies
in IoT and Biomedical Applications”**

1st - 5th December 2025

Name:

Designation:

Institution/Organization:

Address:

Contact Number:

Email:

Edu. Qualifications:

Subjects Taught so far:

No. of FDPs attended:

Experience in years: Teaching:

Industry:

Declaration:

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the coordinator in case, I am unable to attend the course.

DATE:

PLACE:

Signature of the candidate

Signature of the HOD

Address for Communication:

Dr. Ch. Raghavendra

Assistant Professor, Dept. of ECE

VRSSSE, SAHE, Kanuru-520007

Mobile: 9640952001

Email: raghi.2u@vrsiddhartha.ac.in

Dr. MD. Haji Habibulla

Associate Professor, Dept. of ECE

PVPSIT, Kamuru-520007

Mobile: 9246123786

Email: Habibulla@pvpsiddhartha.ac.in

Chief Patron:

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Prof. P. Venkateswara Rao, Vice Chancellor, SAHE

Prof. A.V. Ratna Prasad, Pro Vice-Chancellor, SAHE

Dr. D. Venkata Rao, DEAN, VRSSSE, SAHE

Dr. K. Sivaji Babu, Principal, PVPSIT

Conveners:

Dr. D. Venkata Rao, HOD, ECE, SAHE

Dr. C. Subba Rao, Professor & Head, PVPSIT

Co-Conveners:

Dr. A. Jhansi Rani, Professor Department of ECE, Head
Antenna Research Group, SAHE

Antenna Research Group Organizing Committee:

Dr. M. Santoosh Kumar Singh, Assistant Professor, ECE, VRSSSE, SAHE

Dr. V. Veera Babu, Assistant Professor, ECE, VRSSSE, SAHE

Dr. Iqram Hyder, Assistant Professor, ECE, VRSSSE, SAHE

Mrs. B. Alekhya, Assistant Professor, ECE, VRSSSE, SAHE

Mrs. K. Sneha, Assistant Professor, ECE, VRSSSE, SAHE

Mr. K. Prem Chand, Assistant Professor, ECE, VRSSSE, SAHE

Ms. B. Swathi Lakshmi, Asst. Prof, ECE, PVPSIT

Dr. L. Madhavi Devi, Asst. Prof, ECE, PVPSIT

Mrs. D. Swathi, Asst. Prof, ECE, PVPSIT

Dr. B. Samuyelu, Sr. Asst. Prof, ECE, PVPSIT

Student Coordinators:

Mr. D. Krishna Raju, IV/IV B.Tech, ECE-A ☎ 9440775273

Mr. S. Manas Charan, IV/IV B.Tech, ECE-A ☎ 7013923539

Mr. K. Jishnu Tej, IV/IV B.Tech, ECE-A ☎ 7893057664

Mr. P. Lokesh, IV/IV B.Tech, ECE-A ☎ 8125355866



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An Institution **DEEMED TO BE UNIVERSITY**

(Under Section 3 of UGC Act, 1956)
(Sponsored by Siddhartha Academy of General & Technical Education), Vijayawada, A.P.

In Association with

PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY

Five Day

Faculty Development Program

on

**INTELLIGENT ANTENNA SYSTEMS
AND ELECTROMAGNETIC TECHNIQUES
LEVERAGING AI FOR RF AND
MICROWAVE TECHNOLOGIES IN
IOT AND BIOMEDICAL APPLICATIONS**

1st - 5th December 2025

Sponsored by

Siddhartha Academy of General & Technical Education

Faculty Coordinators:

Dr. Ch. Raghavendra, Assistant Professor, VRSSSE

Dr. V. Saritha, Assistant Professor, VRSSSE

Dr. MD. Haji Habibulla, Associate Professor, PVPSIT

Dr. V. S. D. Rekha, Assistant Professor, PVPSIT



Organized by:

Dept. of Electronics & Communication Engineering

www.siddhartha.edu.in

www.pvpsiddhartha.ac.in

About the university (SAHE):

In 2024, VR Siddhartha Engineering College was declared as a deemed-to-be university under Section 3 of the UGC Act, 1956 by Ministry of Education, Government of India. With a legacy of educational excellence, the institution's UGC-granted autonomy, renewed through 2027-28, provides a strong foundation for its transformation. Guided by a detailed 15-year strategic vision and a 5-year rolling implementation plan, Siddhartha Academy of Higher Education focuses on key areas such as academics, faculty recruitment, student admissions, research, ICT infrastructure, and administration, with specific annual milestones and measurable outcomes. SAGTE, as a responsive educational trust, aligns with the Government of India's target of achieving a 50% GER in higher education by 2035. Siddhartha Academy of Higher Education, Deemed-to-be University, offers diverse undergraduate and postgraduate programs through its specialized schools, including the School of Engineering (B.Tech., M.Tech.), School of Management (MBA), School of Law, School of Science (B.Sc., M.C.A.), and School of Arts & Commerce (B.Com.). Envisioned to support interdisciplinary and multidisciplinary education for students' holistic development, the university aligns with the aspirations and requirements of NEP 2020 to equip students with the skills and knowledge essential for future growth and societal contributions.

About PVPSIT:

Prasad V Potluri Siddhartha Institute of Technology, established in 1998, is a pioneering self-financed institution known for its academic excellence and discipline. The college is sponsored by Siddhartha Academy of General and Technical Education, which manages 18 reputed educational institutions across diverse domains.

About FDP:

This Faculty Development Program "Intelligent Antenna Systems and Electromagnetic Techniques Leveraging AI for RF and Microwave Technologies in IoT and Biomedical Applications", aims to provide participants with an understanding of the design, challenges, and practical applications of compact antennas in these domains. It focuses on equipping participants with the knowledge to innovate in antenna technology, preparing them for the complexities and opportunities in EW and next-generation wireless systems. With the rapid advancement in wireless communication, the integration of intelligent antenna systems and electromagnetic techniques has become crucial for the development of efficient RF and microwave technologies. These technologies play a pivotal role in enabling high-performance IoT devices and cutting-edge biomedical applications. The advent of Artificial Intelligence (AI) further enhances these systems by optimizing signal processing, improving antenna design, and enabling adaptive communication strategies.

Topics Covered:

- Fundamentals of Antenna Systems and Electromagnetic Techniques
- RF and Microwave Technology Basics
- Introduction to AI and Machine Learning Techniques for Communication Systems
- AI-Driven Antenna Design and Optimization
- Applications in IoT: Smart Sensors and Wireless Networks
- Biomedical Applications: Wearable Devices and Medical Imaging
- Case Studies and Future Trends

Eligibility: The FDP is open to Faculty Members, PG Students, Research Scholars, Persons from Industry and R&D organizations across the country.

Resource Persons



Dr. Satya Deep Das
RF Design and life cycle Specialist,
ROSENBERGER, Bangalore.
(DAY 1)



Sri Akula Naresh
Scientist-F & Head, Artificial Intelligence
Defence Research & Development
Laboratory (DRDL) DRDO, Hyderabad.
(DAY 2)



Debaprasad Barad
Research & Development Engineering
ASTRA MICROWAVES, Hyderabad.
(DAY 3)



Dr. DebojithDe
Sr. Engineer,
MERCEDES BENZ Research,
Bangalore
(DAY 4)



Mr. Dharmendra Nayak
Manager Business Development, MMRFIC Pvt. Ltd, Hyderabad.
(DAY 5)

Registration Fee: *****NIL*****

Registration link:

<https://forms.gle/BhjYxsCuhQEJNmKSA>

Last Date for Registration: 25th November 2025

Prasad V. Potluri Siddhartha Institute of Technology
Department of Electronics and Communication Engineering

CIRCULAR

Date: 16-06-2025

This is to inform that the Department of Electronics and Communication Engineering, PVPSIT is conducting a 5-day Hands-on training programme on "Applications of LabVIEW in Engineering" from 01-07-2025 to 05-07-2025 for the faculty of other Engineering colleges. The faculty those who are interested are informed to register for the event.

Venue: DSP Lab (Room No. 255)

Time: 9.00AM-5.00 PM

Co-ordinator & Resource Person: Dr. A. Geetha Devi, Associate professor, ECE


HOD-ECE
HEAD
Electronics & Communication Engg. Dept.
PRASAD V. POTLURI
SIDDHARTHA INSTITUTE OF TECHNOLOGY
KANURU, VIJAYAWADA-520



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Information Services

Memento of Appreciation

Presented to

**Prasad V. Potluri Siddhartha
Institute Of Technology Vijayawada**

*With deep gratitude and sincere appreciation
for graciously hosting the*

IEEE – EBSCO Knowledge Feast

on the theme

Transforming the Future of Research with IEEE

On 20th November 2025

At

Seminar Hall, Ground floor, PVPSIT, Vijayawada

ORGANIZING COMMITTEE

Dr. J. Ravindra Babu

Dr. T. Haritha

Mr. M.Srinivasa Rao

Mrs. K.Anitha

Dr. A. Geetha Devi

Dr. B.S. Prasada Rao

Dr. Md. Habibulla

---Assoc. Professors, Dept. of E.C.E.

Mrs. K.Aruna Kumari

Dr. B.Samuyelu

Mr. T.Balaji

----Sr. Asst. Professors., Dept. of E.C.E.

Ms.T.Sushma

Dr. V.Ratna Kumari

Mrs. D. Hareesha

Mrs. B.V.Subbayamma

Mrs. B.Swathi Lakshmi

Dr. K.P.Rama Krishna

Mr. A.Ashok Babu

Dr. P.Venu Madhav

Dr. V.S.D. Rekha

Dr. L. Madhavi Devi

Mrs. D.Swathi

---- Asst. Professors. Dept. of E.C.E.

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Technical Education

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Siddhartha Academy of General &
Technical Education

Convener

Sri Vellanki Nagabhushana Rao

PVP Siddhartha Institute of Technology

Chairman

Dr. K. Sivaji Babu

Principal

PVP Siddhartha Institute of Technology

Co Chairman

Dr. C. Subba Rao, Prof. & HOD

Department of Electronics & Communication Engg.

Coordinator

Dr. Ch. Gangadhar, Assoc. Professor

Dr. T. Maha lakshmi, Asst. Professor

Department of Electronics & Communication Engg.

Co- Coordinator

D. Praveena Bai, Asst. Professor

Ch. Sree Lakshmi, Asst. Professor

Department of Electronics & Communication Engg.

Five - Day Faculty Development Program on “VLSI Design & Methodologies”

**FDP Dates: 22nd to 26th June 2026
(ONLINE MODE)**

Organized by

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
(Accredited by NBA)**



**Prasad V. Potluri
Siddhartha Institute of Technology**
(AUTONOMOUS) NAAC A+
Approved by AICTE, New Delhi
Permanently Affiliated to JNTUK, Kakinada



ABOUT PVPSIT

Prasad V Potluri Siddhartha Institute of Technology established in 1998 is a pioneering self-financed Institution renowned for Academic excellence and discipline. The college is sponsored by Siddhartha Academy of General and Technical Education (SAGTE), the premier organization that runs 18 Educational Institutions in diverse fields of study. The college offers B.Tech programme in Electronics and Communication Engineering (ECE), Computer Science Engineering (CSE), CSE - Artificial Intelligence and Machine Learning (CSM), CSE - Data Science (CSD), Information Technology (IT), Mechanical Engineering (ME), Electrical and Electronics Engineering (EEE) and CIVIL Engineering (CE); M.Tech programme in ECE, ME and MBA. The college is located at Kanuru in Vijayawada. It is well connected by Rail, Road and Air. Situated in an area of 19.98 acres of Land, the college has all the infrastructural facilities with well-equipped laboratories a Monolithic RCC Structure in a built up area of 36,537sq mts caters to the academic needs of the students.

The Institution is Autonomous, approved by AICTE, permanently affiliated to JNTUK, Kakinada and accredited by NAAC with A+ grade. All UG Programmes are accredited by NBA. It is an ISO 21001-2018 certified institution and is widely acclaimed for its quality standards & UGC accorded 2f / 12B Status. The government of Andhra Pradesh conferred A grade to the institution. The placement cell provides necessary career guidance and enables the students to secure placements. The highest pay package is 44 lakhs. The Institution has MOUs with 60 reputed Industries.

ABOUT THE DEPARTMENT

Welcome to the Electronics & Communication Engineering Department. We cannot envision a world without electronic devices and communication networks, which are now an integral and unavoidable part of our everyday life.

The department's main goal is to prepare its students for real-world issues by providing them with a solid academic foundation in the cutting-edge fields of Electronics & Communication Engineering. The Department features a diverse group of permanent faculty members with 40% Ph.D holders from reputable institutions and combination of youthful and highly experienced. Faculty members exhibit a high degree of commitment and zeal for in teaching and cutting-edge technologies.

The Department is accredited by NBA consecutively 5 times.

The Electronics and Communications Engineering Programme smoothly integrates ideas from fundamental sciences and technology with applicable practice in laboratories and projects to help students develop their problem-solving abilities while keeping in mind the needs of the industry. Signal processing, communication systems, VLSI design, Embedded systems, and IoT are just a few of the topics that students will become proficient in.

The department has experienced faculty and is the greatest asset. Majority of the faculty members are doctorates in diverse areas.

PREAMBLE

The primary goal of the FDP is to promote research and developmental activities to solve problems related to Advanced VLSI (Chip) Logic Design, Physical Design in current VLSI semiconductor era. This creates an opportunity to equip with the ongoing Technological development in the area of VLSI. This FDP offers an excellent platform to the Faculty and Research Scholars helping them to strengthen their knowledge in the domain of VLSI design, Nano Electronics and Image Processing in VLSI.

NO REGISTRATION FEE

VENUE

Department of Electronics & Communication Engg.
Prasad V Potluri Siddhartha Institute of Technology

Affiliated to JNTU Kakinada & Approved by AICTE, New Delhi
Accredited by NBA & NAAC, ISO 9001:2008 Certified Institute
(Siddhartha Academy of General and Technical Education)
Kanuru, Vijayawada- 520 007, A.P.

CONTACT

- Dr. Ch. Gangadhar, Assoc. Professor
& Coordinator, ECE, PVPSIT
Email-id: gangadharch1111@pvpsiddhartha.ac.in
Mobile No: 9440577560
- Dr.T. Maha Lakshmi, Asst. Professor
Email-id: dasari.maha@pvpsiddhartha.ac.in
Mobile No: 9441266701

INVITATION

Automotive & EV Conference 2025

Oct 31 - Nov 1



Insightful keynotes and thought provoking plenaries
curated to guide us confidently into the future of mobility.

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(Autonomous)

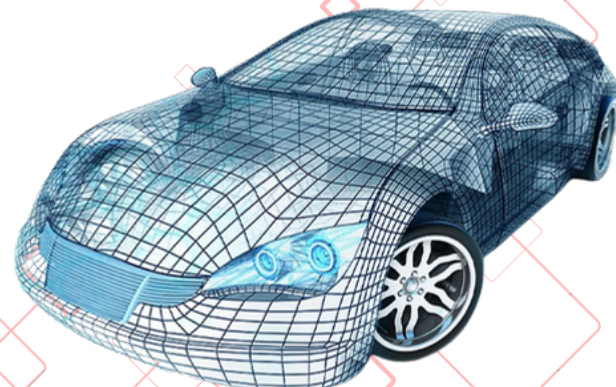
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Technology, Vijayawada





INVITATION

SIDDHARTHA ACADEMY OF HIGHER EDUCATION & PRASAD V. POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY

cordially invites your gracious presence at inauguration of

AUTOMOTIVE & EV CONFERENCE 2025

Sri. PRASANNA RAMMURTHY

President INCOSE

has kindly consented to be the Chief Guest

Distinguished Guests

Sri. D. RAMA KRISHNA

CEO & Founder, Efftronics India Ltd

Sri. J.S.R.K. PRASAD

CII Past chairman, A.P

Guests of Honour

Sri. M. RAJAIAH

President, SAGTE

Sri. P. LAKSHMANA RAO

Secretary, SAGTE

Sri. V. NAGA BHUSHANA RAO

Convenor PVPSIT



Dr. P. VENKATESWARA RAO

Vice Chancellor, SAHE

Dr. K. SIVAJI BABU

Principal PVPSIT



Day 1 Sessions Schedule

<i>Time Slot</i>	<i>Session Title Description</i>	<i>Speaker Details</i>
9- 10 am	Inaugural	
10-10.55 am	The future of Automotive & EVs: Technology trends and Opportunities for India	Hemanth Lohar TUV Rheinland
11:15 – 12:05	INCOSE Session	Prasanna Rammurthy, INCOSE President
12:10 – 13:00	Software-Defined Vehicles (SDV): Transforming Automotive Design & Business Models	Gaurav Dubey Mathwork Incose
14:00 – 14:50	Semiconductors & Electronics in EVs – Role of Chips & Embedded Systems	Vivek Vasantha Renesas
14:55 – 15:45	Automotive IoT & Telematics: Connected Cars and Fleet Monitoring	Yogesh Umbarkar, valtech Mobility
16:05 – 17:30	How can India Build a World-Class Talent Pool	Plenary



Day 2 Sessions Schedule

<i>Time Slot</i>	<i>Session Title Description</i>	<i>Speaker Details</i>
9- 10 am	Systems Engineering for automotive & EVs: From Ideas to Road	Mudit Mittal, INCOSE
10-10.55 am	Functional Safety Design in Automobile	Riya Thakkar, BOSCH
11:15 – 12:05	Battery Pack Design: Chemistry, Safety & Lifecycle	Siva Prasad. G, Comuti Energy
12:10 – 13:00	Design and Ergonomics of EV two wheelers	Saket Nampell, Bajaj Auto Ltd
14:00 – 14:50	Designing the Future of In-Vehicle Passenger Experience	Prashanth Raidu ASHRAE
14:55 – 15:45	EV Charging Standards & Smart Charging	Nishit Kamath, WBG India
16:05 – 16:55	Electric motors and controllers : PMSM, BLDC, induction	Murali Krishna Royal Enfield
17:00 – 18:00	Future of Mobility: Autonomous, Shared & Electric – The Road Ahead	Plenary

Automotive & EV Conference 2025

Oct 31 - Nov 1



To,

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Deemed to be university under Section 3 of UGC Act, 1956



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electronics
To provide insight for enhancing wealth

INCOSE
INDIA



Prasad V Potluri Siddhartha Institute of
Technology, Vijayawada

About IETE

The Institution of Electronics and Telecommunication Engineers (IETE) is India's leading recognized professional society devoted to the advancement of Science and Technology of Electronics, Telecommunication & IT. Founded in 1953. The IETE is the National Apex Professional body of Electronics and Telecommunication, Computer Science and IT Professionals.

The objectives of IETE focus on advancing electro-technology. The IETE conducts and sponsors technical meetings, conferences, symposia, and exhibitions all over India, publishes technical journals and provides continuing education as well as career advancement opportunities to its members.

About IETE Vijayawada Centre

The Institution of Electronics & Telecommunication Engineers (IETE) Vijayawada Centre was established in 1986 as the IETE Vijayawada Local Centre. In the year 1992, it was upgraded to a full-fledged IETE Vijayawada Centre. The Centre has jurisdiction over four districts: Krishna, Guntur, Prakasam, and Khammam. The IETE Vijayawada Centre has over 800 Corporate Members 60 IETE Student Forums (ISFs) 28 IETE Organizational Members More than 10,000 active student members in the region.

IETE Vijayawada Centre proudly announces the establishment of new sub center at Bapatla Womens Engineering College, Bapatla. This milestone has been made possible through the remarkable enrollment of a large number of corporate members and student members, strengthening our technical community and enhancing opportunities for learning, innovation, and professional growth.

Organizing Team

CHIEF PATRON:

- ❖ Shri. Sunil, President IETE, New Delhi
- ❖ Dr. K. Sivaji Babu, Principal, PVPSIT

PATRON:

- ❖ Dr. C. Subba Rao, Prof. & HoD, E.C.E.
- ❖ Dr. B. Surya Prasada Rao, Chairman, IETE, Vijayawada Centre

CONVENER:

- ❖ Dr. Shaik Riyazuddin, Hon. Secretary, IETE, Vijayawada Centre
- ❖ Dr. K. Gnaneshwar Rao, Zonal Mentor (South) & Chairman CAGC, IETE New Delhi

ADVISORY COMMITTEE:

- ❖ Dr. B. Raja Sekhar, Imm. Past Chairman, IETE VJC
- ❖ Dr. Sujith Kalluri, Vice Chairman, IETE VJC
- ❖ Dr. K. Vara Prasad, Hon. Treasurer, IETE VJC
- ❖ Dr. T. Vishnu Priya, Chairperson, WEC, IETE VJC
- ❖ Dr. A. Geetha Devi, Hon. Sec., WEC, IETE VJC

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International Conference on Intelligent Electronics and Communication Systems - 2025 (ICIECS-2025)

Conference Date: 27/12/2025

Venue:

Prasad V. Potluri Siddhartha
Institute of Technology, Kanuru,
Vijayawada-07

Organized by

The Institution of Electronics and
Telecommunication Engineers (IETE)
Vijayawada Centre

In Association with

Department of Electronics and
Communication Engineering

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Golden jubilee Year of Siddhartha Academy of
General & Technical Education



Prasad V. Potluri

Siddhartha Institute of Technology

(AUTONOMOUS) NAAC A⁺

Approved by AICTE, New Delhi

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ISO 21001:2018

About the Institute

Prasad V Potluri Siddhartha Institute of Technology established in 1998 is a pioneering self- financed Institution renowned for Academic excellence and discipline. The college is sponsored by Siddhartha Academy of General and Technical Education (SAGTE), the premier organization that runs 18 Educational Institutions in diverse fields of study. The college offers B.Tech programme in Electronics and Communication Engineering (ECE), Computer Science Engineering (CSE), CSE - Artificial Intelligence and Machine Learning (CSM), CSE - Data Science (CSD), Information Technology (IT), Mechanical Engineering (ME), Electrical and Electronics Engineering (EEE) and CIVIL Engineering (CE); M.Tech programme in ECE, ME and MBA. The college is located at Kanuru in Vijayawada. It is well connected by Rail, Road and Air. Situated in an area of 19.98 acres of Land, the college has all the infrastructural facilities with well- equipped laboratories a Monolithic RCC Structure in a built up area of 36,537sq mts caters to the academic needs of the students.

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It is an ISO 21001:2018 certified institution and is widely acclaimed for its quality standards & UGC accorded 2f / 12B Status. The government of Andhra Pradesh conferred A grade to the institution. NIRF: 200-250.

The institution provides ample facilities for Research, Innovation and Entrepreneurial activities. The placement cell provides necessary career guidance and enables the students to secure placements. The highest pay package is 44 lakhs. The Institution has MOUs with 60 reputed Industries.

About the Department

Welcome to the Electronics & Communication Engineering Department. We cannot envision a world without electronic devices and communication networks, which are now an integral and unavoidable part of our everyday life. The department's main goal is to prepare its students for real- world issues by providing them with a solid academic foundation in the cutting-edge fields of Electronics & Communication Engineering.

The Department features a diverse group of permanent faculty members with 58% Ph.D. holders from reputable institutions and combination of youthful and highly experienced. Faculty members exhibit a high degree of commitment and zeal for in teaching and cutting-edge technologies.

The Department is accredited by NBA consecutively 5 times. The Electronics and Communications Engineering Programme smoothly integrates ideas from fundamental sciences and technology with applicable practice in laboratories and projects to help students develop their problem-solving abilities while keeping in mind the needs of the industry. Signal processing, communication systems, VLSI design, Embedded systems, and IoT are just a few of the topics that students will become proficient in.

The department has 26 experienced faculty members with various areas of specializations in ECE and 15 faculty members completed their Ph.D, 7 faculty members are pursuing their Doctoral degrees. Faculty members conduct research in various disciplines of ECE and are publishing the research outcomes in reputed and indexed (Scopus & SCI) journals and conferences regularly. Innovations by faculty members are filed for patenting and are granted subsequently.

➤ Invited paper submissions in the following major areas but not limited: -

- Internet of Things (IoT)
- Communications
- VLSI Design
- Quantum Computing
- Cyber Security
- Artificial Intelligence
- Bio Medical Engineering
- Machine Learning
- Green Technologies

Paper Template Link

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- Mail your paper to: iciecs2025@pvpsit.ac.in
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