

One-Week STTP on Intelligent Computing & Deep Learning (ICDL-2KI7) 20-25 November, 2017



Organized by

**Department of Computer Science and
Engineering**

Prasad V. Potluri

**Siddhartha Institute of Technology
(Autonomous)**

(Permanent Affiliation to JNTUK, Kakinada)
(Recognized by AICTE, Accredited by NBA and
ISO 9001:2008 Certified Institution)
Kanuru, Vijayawada- 520007

Sponsored By:

**Siddhartha Academy of General
And Technical Education**

Knowledge Partner



Computer Society of India

About the Academy:

Siddhartha Academy of General and Technical Education was established in 1975 and runs 19 educational institutions in and around Vijayawada including two Post Graduate Colleges, Three Under-Graduate Colleges, three Junior Colleges, two Engineering Colleges, two Public Schools, a Medical College, a Dental College, a Nursing School, a Pharmacy College, a Hotel Management College, a Law College and a B.Ed. College. Two of the Under Graduate Colleges and two Engineering Colleges were granted Autonomous status.

About the College:

Prasad V. Potluri Siddhartha Institute of Technology is one of the 19 institutions sponsored by Siddhartha Academy of General and Technical Education (SAGTE). The institution has been established in the year 1998 with a strong mission of imparting quality technical education through continuous non-compromising interactive teaching. The institution offers six graduate programs and six post graduate programs including MCA and MBA. PVPSIT is located in the historic city of Vijayawada on the banks of the Krishna River in the coastal area of Andhra Pradesh. The city is a large commercial centre and famous for Goddess Kanakadurga temple on the Indrakiladri hills. Vijayawada is also known as educational hub in the state of Andhra Pradesh. The city is well connected by road, rail and air. The Railway station, Bus Stand and Airport area with a distance of 10 KM each from college.

About the Department:

The Department of Computer Science and Engineering was formed in the year 1999. The department has started B.Tech program with an initial intake of 40 in the year 1999 and 120 from 2007 onwards. The department has also started M.Tech program with an initial intake of 18 in the

year 2006, and an increase in the intake to 36 in the year 2011. The department is accredited by NBA in the year 2007 for a period of 3 years and was reaccredited in the year 2012 for a period of 2 years and then reaccredited in the year 2016 for a period of 3 years. The department has 34 qualified teaching staff with 6 PhD and 28 M.Techs. The faculty members are involved in research activities and published/presented papers in national and international journals and conferences.

The department will be organizing an International Conference on Smart Computing & Informatics (SCI-2018) during 27~28th January 2018. All accepted and registered papers will be published in Springer SIST series and the books of the series are submitted to SCOPUS and SpringerLink.

The Department is also organizing the First National Level Project Contest "INO BESHON" for CSE, IT, MCA & ECE students during 27~28th January 2018.

For more details please visit:

<http://www.pvpsiddhartha.ac.in>

<http://www.pvpsiddhartha.ac.in/sci2018>

Theme of the Training Program:

The Theme of the Short Term Training Program is to provide a forum for exchanging the ideas and share the knowledge on current Research challenges/issues in the real world with the emerging areas of Intelligent Computing, Deep Learning and Sensor Technologies.

This STTP covers deep understanding of Intelligent Systems includes Deep Learning, Machine Learning, Neural Networks, Evolutionary Computations and Hybrid intelligent systems. These paradigms are comprehensively used in various Science and Engineering streams. This STTP will help the students to gain the knowledge in latest emerging areas of engineering education.

Experts from IIT's and NIT's and Industry are invited.

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2. **Mr. Gautam Mahapatra, Vice President, CSI**
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4. **Mr. Manas Ranjan Pattnaik, Hon-Treasurer, CSI**
5. **Mr. Raju L Kanchibhotla, Chairperson, DIV-III, CSI**

Resource Persons:

1. **Dr. S. K. Udgata, Professor, University of Hyderabad**
2. **Mr. Bhat Dittakavi, CEO, Data Scientist, Variance AI, Hyderabad.**
3. **Mr. V Sunil Kumar, Principal Scientist, Philips, Bangalore.**
4. **Mrs. Pritee Parwekar, Asst. Professor, ANITS**
5. **Dr. Suresh Chandra Satapathy, Professor & Head, Dept of CSE, PVPSIT.**

Registration Fee:

Faculty CSI/IEEE/ACM - Rs.1000/-

Non CSI/IEEE/ACM/Industry -Rs.1500/-

M.Tech/B.Tech Students -Rs.500/-

Registration charges include Certificate of Participation, Working Lunch and High Tea. Registration is limited to 50 participants and is based on First Come First Serve basis. Interested participants are requested to fill the online form.

Registration fee:

Principal, Prasad V Potluri Siddhartha Institute of Technology, Account No:33672200005927, IFSC Code : SYN0003367, Syndicate Bank, VRSEC Branch, Kanuru

The completed Registration Form should be submitted on or before **11-11-2017**

Registration Link : <https://goo.gl/hhDKcA>

More Details: www.pvpsiddhartha.ac.in

ADDRESS FOR CORRESPONDENCE

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Program Schedule:

Day 1:

08.30 -9.30am Registration
09.30 -10.45am Inauguration
10.45 -11.00am Tea Break
11.00 -01.00pm Technical Session-I
01.00-02.00pm Lunch Break
02.00 -04.00pm Technical Session-II
04.00 -05.00pm Research Support

Day 2:

09.00 - 10.45am Technical Session –III
10.45 - 11.00am Tea Break
11.00 - 01.00pm Technical Session- IV
01.00 - 02.00pm Lunch Break
02.00 - 04.00pm Technical Session-V
04.00 - 04.15pm Tea Break
04.15- 05.00pm Research Support

Day 3:

09.00 - 10.45am Technical Session –VI
10.45 - 11.00am Tea Break
11.00 - 01.00pm Technical Session- VII
01.00 - 02.00pm Lunch Break
02.00 - 04.00pm Technical Session-VIII
04.00 - 04.15pm Tea Break
04.15- 05.00pm Research Support

Day 4:

09.00 - 10.45am Technical Session –IX
10.45 - 11.00am Tea Break
11.00 - 01.00pm Technical Session- X
01.00 - 02.00pm Lunch Break
02.00 - 04.00pm Technical Session-XI
04.00 - 04.15pm Tea Break
04.15- 05.00pm Research Support

Day 5:

09.00 - 10.45am Technical Session –XII
10.45 - 11.00am Tea Break
11.00 - 01.00pm Technical Session- XIII
01.00 - 02.00pm Lunch Break
02.00 - 04.00pm Technical Session-XIV
04.00 -04.15pm Research Support

DAY 6

09.00-10.45 Recent Trends in Deep Learning
11.00 – 12.30 ---Valedictory