One day workshop

On

SIMULATION AND DESIGN OF ELECTRICAL CIRCUITS USING MY RIO AND ANALOG DISCOVERY 2

2nd FEBRUARY 2018

Organized by

Department of
Electrical and Electronics Engineering
Prasad V. Potluri Siddhartha Institute of Technology
(Autonomous)
(Permanent Affiliation to JNTUK, Kakinada)
Kanuru, Vijayawada,
Andhra Pradesh-520007
Phone: 0866-2581699

ABOUT THE INSTITUTE

Prasad V. Potluri Siddhartha Institute of Technology is one of the 19 institutions sponsored by Siddhartha Academy of General and Technical Education (SAGTE). The institution has been established in the year 1998 with a strong mission of imparting quality technical education through continuous non-compromising interactive teaching. It is permanently affiliated to Jawaharlal Nehru Technological University, Kakinada. As a result of its continuous efforts to attain quality, it conferred Autonomous status in 2012 and certified by ISO 9001:2008. The college is accredited by National Assessment and Accreditation Council and NBA.

The institution offers six graduate programs and six post graduate programs including MCA and MBA. The college stands as a symbol of academic excellence with the departments headed by the eminent personalities. Students at this college are trained to transform into a full-fledged individual by practicing a strong ethical code.

ABOUT THE DEPARTMENT

The department offers Under Graduate program in Electrical and Electronics Engineering with the intake of 120 and Post Graduate program in Power System & Control with an intake of 18. The department has Twenty eight faculty members supported by 13 Technical and Administrative Staff.

The Department is well equipped with 12 laboratories. Department has the latest software - MATLAB, PSCAD, PSIM, PSPICE, Mi Power. The faculty of the Department has been working rigorously by undertaking advanced research projects

ABOUT THE WORKSHOP

The workshop provides a training to the scholars / Faculty / Students in myRIO and ANALOG DISCOVERY KITS. The myRIO Student Embedded Device features I/O on both sides of the device in the form of MXP and MSP connectors.

It includes analog inputs, analog outputs, digital I/O lines, LEDs, a push button, an onboard accelerometer, a Xilinx FPGA, and a dual core ARM Cortex A9 processor. The Analog Discovery 2 transforms any PC into an electrical engineering workstation. This USB-powered device enables students to build and test analog and digital circuits in any environment with the functionality of traditional benchtop instruments. In addition to the 100 MS/s two-channel oscilloscope, the Analog Discovery 2 provides a two-channel waveform generator, 16-channel logic analyzer, 16-channel digital pattern generator, Spectrum analyzer, Network analyzer, voltmeter, and ±5 VDC adjustable power supplies.

ELIGIBILITY

Faculty members, PG Students and Research Scholars of EEE and ECE Departments from AICTE/UGC recognized Engineering Institutions/ Industries are eligible to participate in this workshop.

Note: Confirm your participation through e-mail.

No registration fee.

RESOURCE PERSON

Mr. Rajkumar Rajasekaran
Application Engineer
National Instruments, Bangalore.

CONVENOR

Dr. M.Venu Gopala Rao
Professor & Head
EEE Department, PVPSIT

CO-ORDINATORS

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REGISTRATION FORM

Name:

Designation:

Organization:

Address:

Mobile No :

E-mail :

Participation Category:

(Faculty/ Industry/ R&D)

Place:

Date: Signature of the Candidate

SPONSORSHIP CERTIFICATE

Mr./Ms. is an employee of our Organization and is here by sponsored to the training program.

He / She is permitted to participate in the training if selected.

Place:

Date: Signature of Sponsoring Authority
(With seal)