Prasad V Potluri Siddhartha Institute of Technology (Autonomous)
Kanuru, Vijayawada

Department of Computer Science & Engineering

# CIRCULAR

PVPSIT/CSE/CIR./2023

DATE: 24-02-2023.

Entrepreneurship Development Cell of CSE is going to conduct one day workshop on "Design Thinking" for interested students of II B. Tech. This session will be addressed by Cohort 2021 & 2022 UIF students of PVPSIT and VVIT. Students can register their names with the faculty coordinator Mr. A. Prashant at Room No: 223 before 28-02-2023 12:00 pm.

**Date & Venue:**

02-03-2023 – 09:00 am to 04:00 pm - AUDITORIUM

**Coordinator (UIF) & (EDC):**

Mr. A. Prashant, Asst. Prof., Dept. of CSE, 8019709941

**Note:** Please ignore if already registered during UIF Awareness Program.

**Copy to HoDs:**

HoD, CE –

HoD, ECE –

HoD, EEE –

HoD, ME –

HoD, IT –

24/02/2023.

(Dr. A. Jayalakshmi)

**Prof. & Head, Dept. of CSE**

HEAD
Dept.of Computer Science & Engg.
PRASAD V.POTLURI
SIDDHARTHA INSTITUTE OF TECHNOLOGY
KANURU,VIJAYAWADA-520007.

03-03-2023

To,

The HoD,

Department of CSE,

PVPSIT.

Respected Madam,

Sub: Grant of Attendance for students – Design Thinking Workshop – EDC – Regd.

It is to bring to your kind notice that, ED Cell Department of CSE organised Design Thinking Workshop on 02-03-2023 by UIF students of PVPSIT and VVIT. The following students have participated in the above said program. Hence, I request you to kindly grant attendance for the below mentioned list of students, for the complete day (9:00 am to 5:00 pm) on 02-03-2023.

| S. No. | Branch | Year | Section | Regd. No. | Name |
|---|---|---|---|---|---|
| 1 | CSE | 2 | S1 | 21501A0526 | Ch. Yogeesh Sai |
| 2 | CSE | 2 | S3 | 21501A05E5 | P. KAVYA SRI |
| 3 | CSE | 2 | S3 | 21501A05G5 | SOMU LIKITHA |
| 4 | CSE | 2 | S3 | 21501A05H1 | T SRIJA |
| 5 | CSE | 2 | S3 | 22505A0514 | S. SURYA MOUNIKA |
| 6 | CSE | 3 | S1 | 20501A0513 | Banavath.Srividhya |
| 7 | CSE | 3 | S1 | 20501A0564 | Guvvala Aparna |
| 8 | CSE | 3 | S1 | 20501A0542 | EEDARA VIJAYALAKSHMI |
| 9 | CSE | 3 | S1 | 20501A0541 | DUPPATI SRAVANI |
| 10 | CSE | 3 | S1 | 21505A0503 | Ch Sonushruthi |
| 11 | CSE | 3 | S1 | 21505A0502 | HEMA PRIYA BOBBILI |
| 12 | CSE | 3 | S1 | 20501A0559 | Deepika Gullapalli |
| 13 | CSE | 3 | S2 | 20501A05B7 | Mopidevi Karthik |
| 14 | CSE | 3 | S2 | 20501A05C6 | Myneni Bhaavan |
| 15 | CSE | 3 | S2 | 20501A0569 | Jonnalagadda Dinesh |
| 16 | CSE | 3 | S2 | 20501A0583 | KAZA SIVA NAGA UDAY KIRAN |
| 17 | CSE | 3 | S2 | 20501A05C8 | Nagulla Paurnavi Yadav |
| 18 | CSE | 3 | S2 | 20501A05C0 | Mummadivarapu Revathi |
| 19 | CSE | 3 | S2 | 20501a0589 | KOMMU JOSHNA RANI |
| 20 | CSE | 3 | S2 | 21505A0509 | K.Pavan Kumar |
| 21 | CSE | 3 | S2 | 20501A0591 | Koneru Chaitanya Chandra |
| 22 | CSE | 3 | S2 | 20501A05B3 | Merakanapalli Jayanth Nageswara Rao |
| 23 | CSE | 3 | S3 | 20501A05H6 | VALLURI SRI HARSHA |
| 24 | CSE | 3 | S3 | 20501A05F2 | Salopantula Mani Sindhura |
| 25 | CSE | 3 | S3 | 21505A0514 | Nelakurthi Poojitha |

| | | | | | |
|---|---|---|---|---|---|
| 26 | CSE | 3 | S3 | 20501A05I2 | Veeramachaneni Harsha Lokh |
| 27 | CSE | 3 | S3 | 20501A05D2 | Nanduri Rohitha |
| 28 | CSE | 3 | S3 | 21505A0517 | Suda Krishna Priya |
| 29 | CSE | 3 | S3 | 20501A05J0 | Yarlagadda Hari Krishna Vamsi |
| 30 | CSE | 3 | S3 | 20501A05D3 | NARAYANA TEJA |
| 31 | CSE | 3 | S3 | 20501A05F1 | Rayapati Sasank Sai |
| 32 | CSE | 3 | S3 | 20501A05E9 | R KOTI NARASIMHA |
| 33 | CSE | 3 | S3 | 20501A05F9 | SIRIYALA SURAJ |
| 34 | CSE | 3 | S3 | 20501A05F7 | SIMHADRI JAYA CHANDER |
| 35 | CSE | 3 | S3 | 20501A05F8 | S Eswar Sai Reddy |

A. Prashant 3/3/23

Mr. A. Prashant

Assistant Professor
Department of CSE
EDC Coordinator.

To
Dept. Office
Pl. do needful

03/03/23.

HEAD
Dept. of Computer Science & Engg.
PRASAD V. POTLURI
SIDDHARTHA INSTITUTE OF TECHNOLOGY
KANURU, VIJAYAWADA-520007.

03-03-2023

To,

The HoD,

Department of EEE,

PVPSIT.

Respected Sir,

Sub: Grant of Attendance for students – Design Thinking Workshop – EDC Department of CSE – Regd.

It is to bring to your kind notice that, ED Cell Department of CSE organised Design Thinking Workshop on 02-03-2023 by UIF students of PVPSIT and VVIT. The following students have participated in the above said program. Hence, I request you to kindly grant attendance for the below mentioned list of students, for the complete day (9:00 am to 5:00 pm) on 02-03-2023.

| S. No. | Branch | Year | Section | Regd. No. | Name |
|---|---|---|---|---|---|
| 1 | EEE | 2 | S1 | 21501A0202 | ABDUL AMEEN |
| 2 | EEE | 2 | S1 | 21501A0214 | CH. JAGADISH |
| 3 | EEE | 2 | S1 | 21501A0215 | NAVEEN CHARAGONDA |
| 4 | EEE | 2 | S1 | 21501A0221 | E. ANANTH KUMAR |
| 5 | EEE | 2 | S1 | 21501A0228 | G. GOWTHAM |
| 6 | EEE | 2 | S1 | 21501A0240 | K. TEJA |
| 7 | EEE | 2 | S1 | 21501A0243 | K. SASWANTH |
| 8 | EEE | 2 | S1 | 22505A0203 | B. YASWANTH KUMAR |
| 9 | EEE | 2 | S2 | 22505A0217 | R. LEELA VENKATA KUMAR |
| 10 | EEE | 2 | S2 | 22505A0215 | P. Sampath Babu |

A Prashant 3/3/23
Mr. A. Prashant

Department EDC Coordinator.

Jayal 03/03/2023
Dr. A. Jayalakshmi

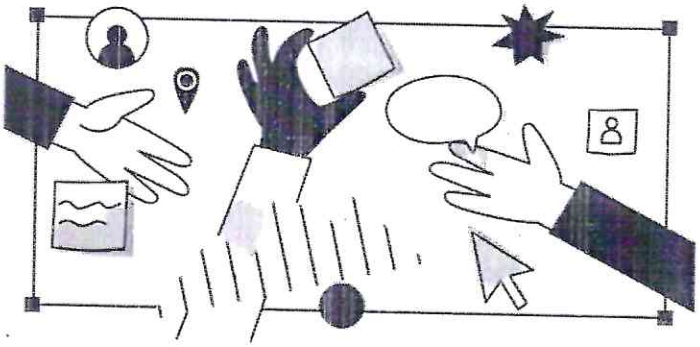
HEAD
Department of
Computer Science & Engineering
PVP Siddhartha Institute of Technology
Kanuru, Vijayawada - 520 007.

university innovation fellows

universityinnovationfellows.org

# WORKSHOP ON

# Design thinking



## ON 02.03.2023

## ALL INTERESTED ONES ARE INVITED!!

TIMMINGS : 9.00AM - 5.00PM

VENUE : AUDITORIUM

**PVPSIT**

PRASAD.V.POTLURI INSTITUTE OF TECHNOLOGY

VVIT

VASIREDDY VENKATADRI INSTITUTE OF TECHNOLOGY

# Workshop on Design Thinking

The workshop was organised by Entrepreneurship Development Cell, Department of Computer Science and Engineering, PVPSIT in collaboration with UIF (University Innovation Fellows) students of PVPSIT and VVIT at Auditorium from 09:00 am to 5:00 pm on 2nd March 2023.

The following UIF students have organised this workshop:

- Dhanya Sree Tottempudi, III CSE S3, 20501A05G9, PVPSIT
- Veenaa Sree Sai Namana, III CSE S3, 20501A05D0, PVPSIT
- Naga Raghavendra Pilli, III CSE S3, 20501A05E7, PVPSIT
- Sri Chandan Vallabhaneni, II CSE S3, 21501A05G6, PVPSIT
- Venkata Subbarao Mupparaju, IV ME, 20BQ5A0321, VVIT
- Madan Mohan Chunduri, IV CSE, 19BQ1A0537, VVIT
- Bhavya Kolluru, IV CSE, 19BQ1A0518, VVIT
- Dharani Buddha, IV ECE, 19BQ1A0421, VVIT
- Vihari Lanka, III EEE, 20BQ1A0272, VVIT
- Vikranth Reddimasu, III AI & DS, 20BQ1A5446, VVIT
- Vivek Grandhi, III CSE(IOT), 20BQ1A4915, VVIT
- Pavithra Srungavarapu, III CSE, 20BQ1A05M4, VVIT
- Anirudh Nunna, II CSE(AI&ML), 21BQ1A42D4, VVIT
- Durga Pranathi Nerella, II AI & DS, 21BQ1A5438, VVIT

The workshop aimed to introduce students to Design Thinking. The workshop helps to develop creative thinking and analytical decision making and develops critical thinking in students. Also aids in skill - based learning to improve collaboration, communication to develop their thinking spectrum.

The workshop began with an introduction to Design Thinking and what are the uses of Design Thinking in real life. Then what are the stages of Design thinking, explaining about what is Define, Empathy, Ideate, Prototype and Testing .Students are divided into smaller batches randomly. Later students are provided with some kits which helps them to make prototypes and make note of other details throughout the workshop. In the mean while mini games were conducted to the students to make them enjoyable throughout the workshop and provided beverages to the students. Total 45 students have participated in the workshop where 35 are from CSE and 10 are from EEE.

5 Stages of Design Thinking are emphasised as follows:

Define: In define stage students are provided with basic ideas and encouraged to identify real world problems accordingly and frame it into a problem statement.

Empathy: In empathy stage they got empathized from different perspectives according to their problem statement and identified the pros & cons of the problem statement.

Ideate: After completing the empathy stage they are having sufficient knowledge about the problem statement and in ideate stage they will generate ideas to solve the real world identified in the define stage of design thinking .

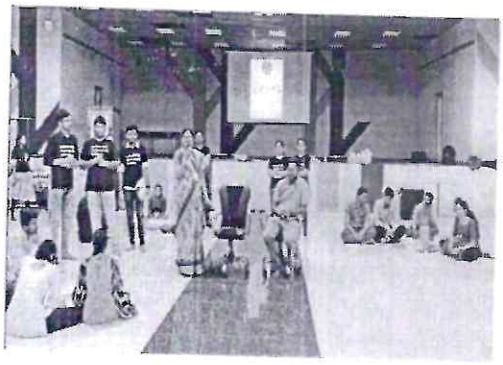
Prototype: In Prototype stage they developed a simple prototype for their problem statement with the ideas developed in the ideate phase.

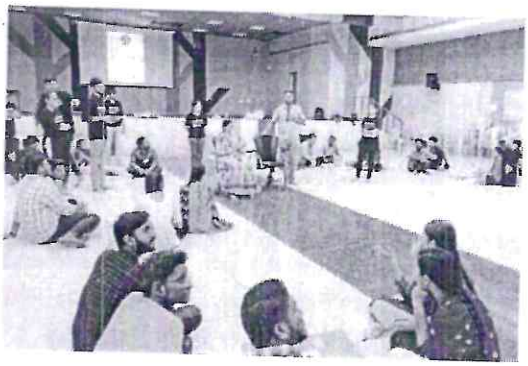
Test: In the testing phase Dr. P. Naga Srinivasu, Associate Professor, Department of CSE acted as a judge and tested prototypes of all the teams. He has given feedback to the teams on the areas where improvements can happen to make their design more effective.

The workshop was well – received by the students, who showed great interest in identifying the problem statement and developing a prototype. Many of them expressed their enthusiasm to make things. Every one learnt how to work as a team and with in short span of time they worked very well together as a team and understood each other very well which in turn gave a good result.

The student teams were awarded with winner and runner prizes by Dr. A. Jayalakshmi, HoD, Department of CSE; Dr. P. Naga Srinivasu, Associate Professor, Department of CSE and Mr. A. Prashant, Assistant Professor and Faculty Champion UIF, Department of CSE. The winners and runners are awarded with the books and certificates. Each and every student who attended the workshop are aware of what is design thinking and how does it makes an impact.

Dr. A. Jayalakshmi, HoD, Department of CSE; Dr. P. Naga Srinivasu, Associate Professor, appreciated the entire UIF team for organising workshop in an effective and planned way. They also encouraged and appreciated the efforts rendered by the participants for being energetic throughout the day.





**FIRST PRIZE TO TEAM "#INCLUDE"**

Mr. K. Jaswanth, EEE
Ms. A. Ameen, EEE
Mr. B. Yaswanth, EEE
Ms. P. Kavyasri, CSE
Ms. G. Deepika, CSE

**SECOND PRIZE TO TEAM "RED HEART"**

Ms. S. Krishna Priya, CSE
Mr. Ch. Naveen, EEE
Mr. E. Ananth Kumar, EEE
Mr. Jayanth Nageswar Rao, CSE

SIGNATURE OF EDC COORDINATOR 21/3/23

SIGNATURE OF HOD 21/03/23

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WORKSHOP ON
Design Thinking
ON 02.03.2023
ALL INTERESTED ONES ARE INVITED!!
PVPSIT
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GPS Map Camera
Vijayawada, Andhra Pradesh, India
Prasad V Potluri Siddhartha Institute of Technology,Vijayawada, FMPV+W9W, Chalasani Nagar, Kanuru, Vijayawada, Andhra Pradesh 520007, India
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WORKSHOP ON
Design Thinking
ON 02.03.2023
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PVPSIT
university innovation fellows
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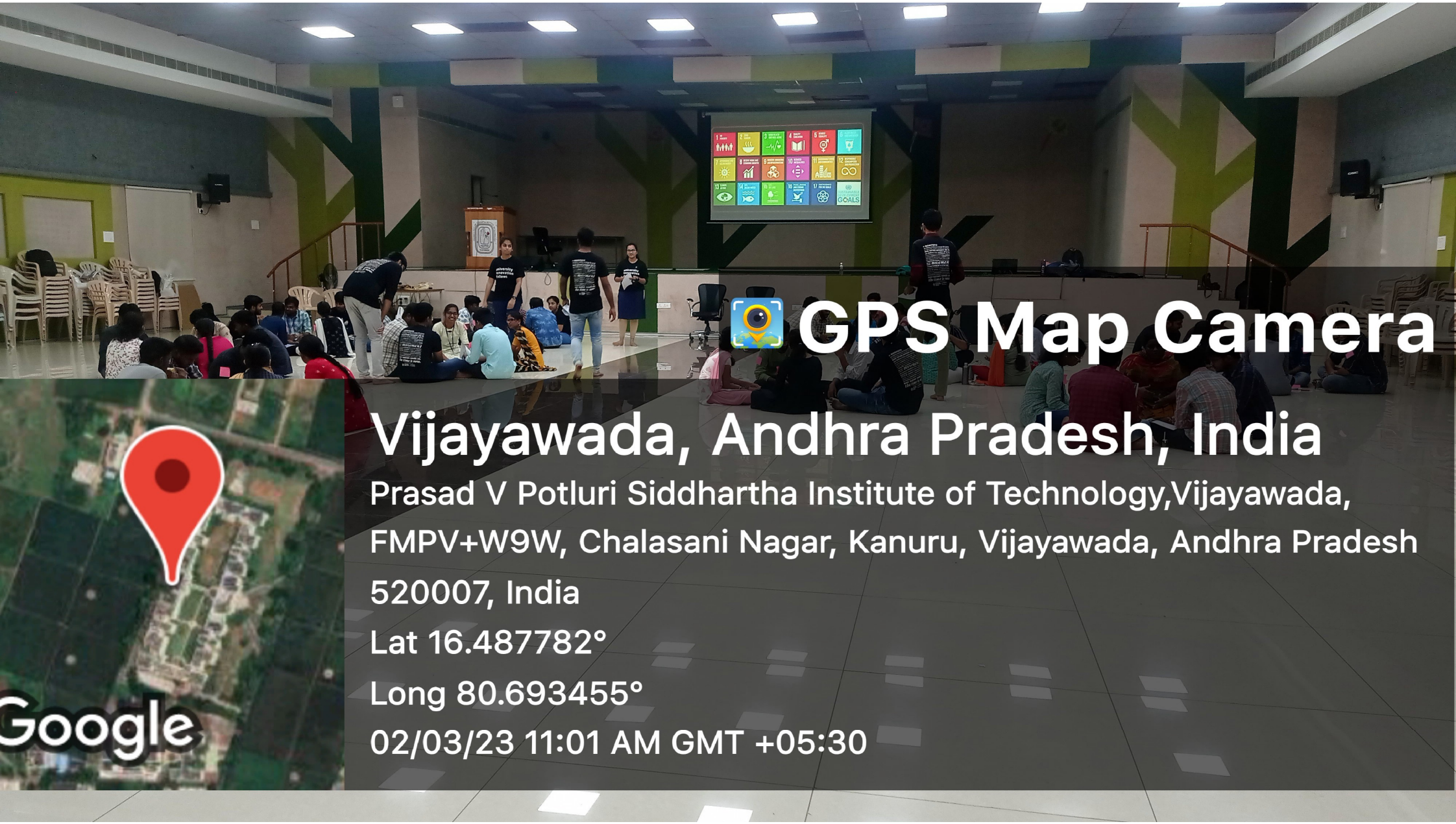
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GPS Map Camera
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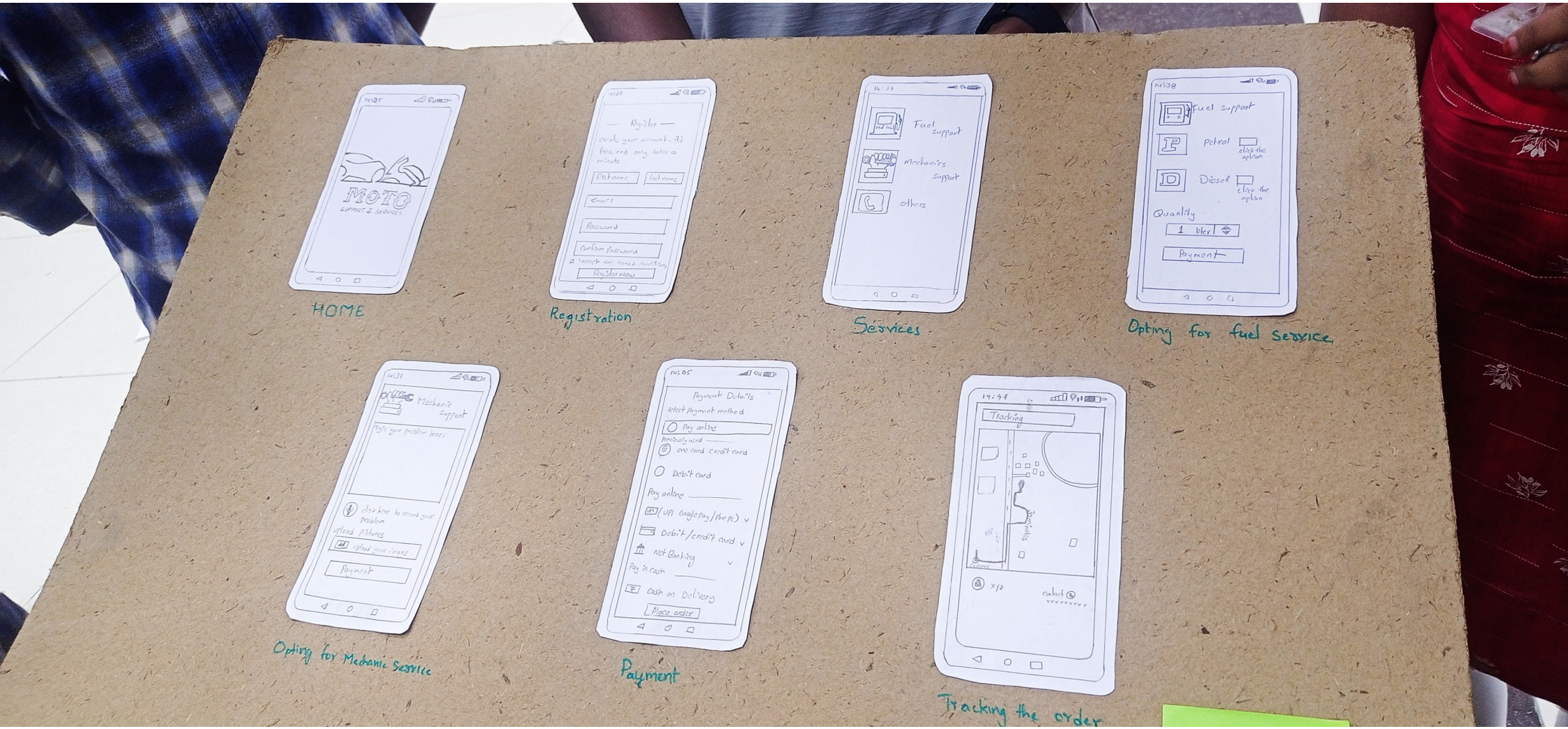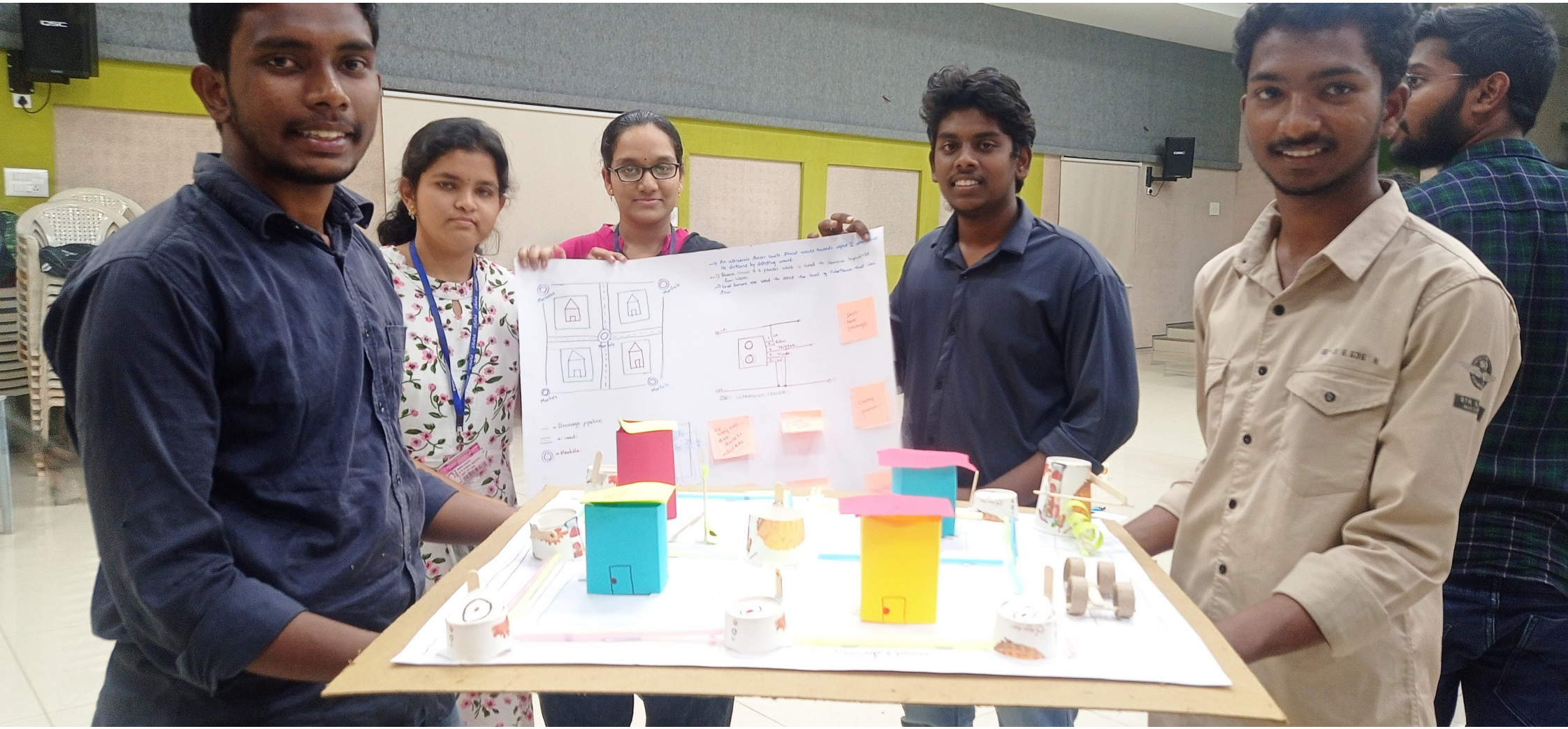
MOTO
SUPPORT & SERVICES
HOME
Register
Create your account. It's free and only takes a minute
First name
Last name
Email
Password
Confirm Password
I accept the terms & conditions
Register Now
Registration
Fuel support
Mechanics support
others
Services
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Petrol
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Diesel
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Quantity
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Payment
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Upload Pictures
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Previously used
one card credit card
Debit card
Pay online
(UPI Google pay/phonepe)
Debit/credit card
Net Banking
Pay in cash
Cash on Delivery
Place order
Payment
Tracking
Tracking the order