

**PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

II - B. TECH II-SEM (S1A)

FOR THE ACADEMIC YEAR 2021-21

ROOM NO: 242A

W.E.F. 06-04-2021

PERIOD	1	2	3	L U N C H B R E A K	4	5	6
DAY	9.00-10.00	10.00-11.00	11.00-12.00		1.00-2.00	2.00-3.00	3.00-4.00
MON	EM-I	AC	NM&PS		EMF	ES	NM&PS
TUE	DLD	NM&PS	LSE		LSE LAB		ES
WED	ES	DLD	EMF		DLD LAB		
THU	AC	EM-I	EMF		EM-I	NM&PS	AC
FRI	EMF	AC	DLD		AC LAB		
SAT	EM-I LAB				LSE	DLD	EM-I

SUBJECT NAME

NAME OF THE FACULTY

Engineering Mathematics - IV (NM&PS)	Sri.A.C.S.Babu
Life Sciences for Engineers (LSE)	M.Anupama Ammulu
Electrical Machines - I (EM-I)	Sri.M.Hemanth Sai
Digital Logic Design (DLD)	Smt.R.Swathi
Analog Circuits (AC)	Smt.V.S.D.Rekha
Electromagnetic Fields (EMF)	Smt.J.Hema Latha
Enviromental Sciences	S.Laxmi Tulasi
Life Sciences for Engineers Lab (LSE Lab)	M.Anupama Ammulu
Electrical Machines -I Lab (EM-I Lab)	Sri.M.Hemanth Sai / Smt.D.Ragaleela / Sri. B. Bala Sai Babu / Smt. B. Lalitha
Digital Logic Design Lab (DLD Lab)	Sri.P.Karunakar / Smt.R.Swathi / Smt. V. Harika
Analog Circuits Lab (AC Lab)	Smt.V.S.D.Rekha / Smt. D. Swathi

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W.E.F. 06-04-2021

PERIOD	1	2	3	L U N C H B R E A K	4	5	6
DAY	9.00-10.00	10.00-11.00	11.00-12.00		1.00-2.00	2.00-3.00	3.00-4.00
MON	EM-I	AC (BL)	NM&PS (BBS)		EMF	ES (MS)	NM&PS (BM)
TUE	DLD (BBN)	NM&PS (VSGL)	LSE (GM)		LSE LAB		ES (RS)
WED	ES (TSR)	DLD (BBS)	EMF		AC LAB		
THU	AC (MDR)	EM-I	EMF		EM-I	NM&PS (TSR)	AC (YVM)
FRI	EMF	AC (MRK)	DLD (MDR)		EM-I LAB		
SAT	DLD LAB				LSE (MRK)	DLD (VSGL)	EM-I

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NAME OF THE FACULTY

Engineering Mathematics - IV (NM&PS)	Sri.A.C.S.Babu
Life Sciences for Engineers (LSE)	M.Anupama Ammulu
Electrical Machines - I (EM-I)	Dr.C.Kumar
Digital Logic Design (DLD)	Smt.R.Swathi
Analog Circuits (AC)	Smt.V.S.D.Rekha
Electromagnetic Fields (EMF)	Dr.M.V.Ramesh
Enviromental Sciences	S.Laxmi Tulasi
Life Sciences for Engineers Lab (LSE Lab)	M.Anupama Ammulu
Electrical Machines -I Lab (EM-I Lab)	Sri.M.Hemanth Sai / Smt.D.Ragaleela / Sri. B. Bala Sai Babu / Smt. B. Lalitha
Digital Logic Design Lab (DLD Lab)	Sri.P.Karunakar / Smt.R.Swathi / Smt. V. Harika
Analog Circuits Lab (AC Lab)	Smt.V.S.D.Rekha / Smt. D. Swathi

**PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY
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II - B. TECH II-SEM (S2A)

FOR THE ACADEMIC YEAR 2020-21

ROOM NO: 262

W.E.F. 06-04-2021

PERIOD	1	2	3	L U N C H B R E A K	4	5	6
DAY	9.00-10.00	10.00-11.00	11.00-12.00		1.00-2.00	2.00-3.00	3.00-4.00
MON	NM&PS	EMF	LSE		EM-I LAB		
TUE	ES	EMF	DLD		AC LAB		
WED	EM-I	AC	EM-I		NM&PS	LSE	AC
THU	EMF	DLD	ES		DLD LAB		
FRI	AC	EM-I	ES		DLD	NM&PS	EMF
SAT	DLD	LSE LAB			EM-I	AC	NM&PS

SUBJECT NAME	NAME OF THE FACULTY
Engineering Mathematics - IV (NM&PS)	Sri.A.C.S.Babu
Life Sciences for Engineers (LSE)	S.Laxmi Tulasi
Electrical Machines - I (EM-I)	Dr.C.Kumar
Digital Logic Design (DLD)	Smt.R.Swathi
Analog Circuits (AC)	Smt.T.Maha Lakshmi
Electromagnetic Fields (EMF)	Dr.M.V.Ramesh
Enviromental Sciences	Smt.T.Preethi Rangamani
Life Sciences for Engineers Lab (LSE Lab)	Smt.S.Laxmi Tulasi
Electrical Machines -I Lab (EM-I Lab)	Sri.C.Kumar / Smt. G. Madhavi / Sri.T.Srinivasa Rao
Digital Logic Design Lab (DLD Lab)	Smt.R.Swathi / Dr. N. Vijaya Anand
Analog Circuits Lab (AC Lab)	Smt.T.Maha Lakshmi / Sri. J. Ravindra Babu

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DAY	9.00-10.00	10.00-11.00	11.00-12.00		1.00-2.00	2.00-3.00	3.00-4.00
MON	NM&PS (YVM)	EMF	LSE (MHS)		DLD LAB		
TUE	ES (MS)	EMF	DLD (BBN)		EM-I LAB		
WED	EM-I	AC (KB)	EM-I		NM&PS (KB)	LSE (GM)	AC (MHS)
THU	EMF	DLD (BM)	ES (BL)		AC LAB		
FRI	AC (RS)	EM-I	ES (TNP)		DLD (PK)	NM&PS (VH)	EMF
SAT	DLD (TNP)	LSE LAB			EM-I	AC (VH)	NM&PS (PK)

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Life Sciences for Engineers (LSE)	S.Laxmi Tulasi
Electrical Machines - I (EM-I)	Sri.M.Hemanth Sai
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