

PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
II - B. TECH I-SEM (S1)
FOR THE ACADEMIC YEAR 2020-21

W. E. F. : 17.08.2020

PERIOD	1	2	3	B R E A K	4	5	6	
DAY	9:15-10:15	10:30-11:30	11:45-12:45			2:00-3:00	3:15-4:15	4:30 - 5:30
MON	SS	ED&AC	ECA			EM-III	ED&AC	ECA
TUE	EM-III	SS	AIT			ECA	COI	ED&AC
WED	AIT	EM-III	ED&AC			DT	SS	EM-III
THU	DT	EM-III	ECA			ED&AC	COI	SS
FRI	ECA	SS	DT			EDC LAB		AIT
SAT	ED&AC	ECA	SS			EM-III	COI	
	9:15-11:15		11:30-1:30		2:30-4:30			
SUN	ECA LAB		DT LAB		AIT LAB			

SUBJECT NAME	NAME OF THE FACULTY
ENGINEERING MATHEMATICS-III (EM-III)	Sri.K.KIRAN KUMAR
AI TOOLS (AIT)	Dr.G.LALITHA KUMARI
DESIGN THINKING (DT)	Smt.M.RADHA DEVI
ELECTRICAL CIRCUIT ANALYSIS (ECA)	Sri.M.HEMANTH SAI
ELECTRONIC DEVICES AND AMPLIFIER CIRCUITS (ED&AC)	Sri.P.KARUNAKAR
SIGNALS AND SYSTEMS (SS)	Smt.J.HEMALATHA
CONSTITUTION OF INDIA (COI)	Dr. KONERU ANURADHA
AI TOOLS LAB (AIT LAB)	Smt.G.LALITHA KUMARI
DESIGN THINKING LAB (DT Lab)	Smt.M.RADHA DEVI/Sri.T.J.PRASANNA KUMAR
ELECTRICAL CIRCUIT ANALYSIS LAB (ECA LAB)	Dr.N.VIJAYA ANAND / Sri.M.HEMANTH SAI
ELECTRONIC DEVICES AND AMPLIFIER CIRCUITS LAB (ED&AC LAB)	Sri.P.KARUNAKAR / Smt.R.SWATH

PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
II - B. TECH I-SEM (S2)
FOR THE ACADEMIC YEAR 2020-21

W. E. F. : 17.08.2020

PERIOD	1	2	3	B R E A K	4	5	6	
DAY	9:15-10:15	10:30-11:30	11:45-12:45			2:00-3:00	3:15-4:15	4:30 - 5:30
MON	DT	SS	EM-III			ED&AC	ECA	AIT
TUE	EM-III	ED&AC	ECA			SS	COI	SS
WED	SS	DT	ED&AC			AIT	EM-III	ECA
THU	ED&AC	ECA	SS			EM-III	COI	ED&AC
FRI	DT	EM-III	AIT			ECA	SS	EM-III
SAT	ECA	ED&AC LAB				ED&AC	COI	
	9:15-11:15		11:30-1:30		2:30-4:30			
SUN	AIT LAB		ECA LAB		DT LAB			

SUBJECT NAME	NAME OF THE FACULTY
ENGINEERING MATHEMATICS-III (EM-III)	Sri.A.CHANDRA SEKHAR BABU
AI TOOLS (AIT)	Dr.G.LALITHA KUMARI
DESIGN THINKING (DT)	Smt.M.RADHA DEVI
ELECTRICAL CIRCUIT ANALYSIS (ECA)	Sri.B.BALA SAI BABU
ELECTRONIC DEVICES AND AMPLIFIER CIRCUITS (ED&AC)	Smt.R.SWATHI
SIGNALS AND SYSTEMS (SS)	Smt.V.S.G.LAKSHMI
CONSTITUTION OF INDIA (COI)	Dr. KONERU ANURADHA
AI TOOLS LAB (AIT LAB)	Smt.G.LALITHA KUMARI
DESIGN THINKING (DT Lab)	Smt.M.RADHA DEVI/Sri.T.J.PRASANNA KUMAR
ELECTRICAL CIRCUIT ANALYSIS LAB (ECA LAB)	Sri.B.BALA SAI BABU / Smt.M.DEVIKA RANI
ELECTRONIC DEVICES AND AMPLIFIER CIRCUITS LAB (ED&AC LAB)	Smt.R.SWATHI / Sri.P.KARUNAKAR