Board ob studies meeting ob EEE Department is held on 06-09-2021 at 3 pm - through online (400gle meet)

PRASAD V. POTLURI SIDDARTHA INSTITUTE OF TECHNOLOGY:: VIJAYAWADA Department of Electrical & Electronics Engineering

Electrical & Electronics Engineering Board of Studies meeting have conducted on 6th September 2021 at 3 PM through online. (Google Meet).

Members present:

1.	Dr. M Venu Gopala Rao, HoD-EEE, PVPSIT	 Chairman
2.	Dr. P V Ramana Rao, Rtd. Professor NIT, Warangal	 Member
3.	Dr. M Sydulu, Professor, NIT, Warangal	 Member
4.	Dr. K Siva Kumar, Assoc. Professor, IIT, Hyderabad	 Member
5.	Dr. M Krishna Murthy, Rtd. Chief Engineer, APGENCO	 Member
6.	Dr. MV Ramesh, Assoc. Professor, PVPSIT	 Member
7.	Dr. P Muthu Kumar, Assoc. Professor, PVPSIT	 Member
8.	Dr. N Vijaya Anand, Assoc. Professor, PVPSIT	 Member
9.	Dr. C Kumar, Assoc. Professor, PVPSIT	 Member
	Mrs. J Hema Latha, Sr. Asst. Professor, PVPSIT	 Member
	Mr. Y Vishnumurthulu, Asst. Professor, PVPSIT	 Member

Agenda:

- Review the minutes of previous meeting
- ❖ Discuss and approve the Four Year B.Tech (EEE) Course Structure and Syllabi of II Year B.Tech courses as per PVP 20 Regulation.

Resolutions:

Reviewed and approved the Four Year Course Structure for B.Tech (EEE) programme and Syllabi of II Year courses as per PVP 20 Regulation.

Dept. of Electrical & Electronics Engg.
PRASAD V.POTLURI

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