

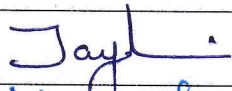
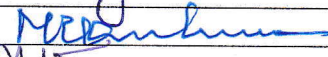



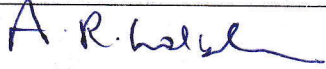

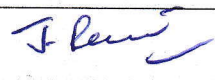
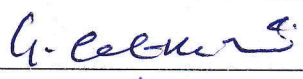
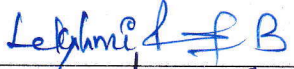
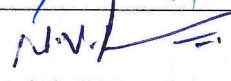

**PRASAD V POTLURI SIDDHARTHA INSTITUTE OF
TECHNOLOGY (AUTONOMOUS)**
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
Minutes of BOS Meeting Held on 03-09-2021

The Board of Studies online meeting (**Google Meet**) is held on 03-09-2021.

The agenda of the meeting is:

1. Review and Approval of PVP20 Regulations - II B.Tech - CSE Course Structure and Syllabus.
2. Any other item with the permission of the Chairman.

The following External & Internal members of Board of Studies attended the meeting:

Sl. No.	Name of the Committee Member	Designation	Signature
1	Prof. D.V.L.N. Somayajulu	Director, IIITDM, Kurnool, Andhra Pradesh	Attended through Online mode (Google Meet)
2	Dr. V Sunil Kumar	Director- Data Science, Ericsson Global Accelerator (GAIA), Bangalore	
3	Mr. Pratap Kolla	Senior Consultant, TCS, Telangana	
4	Dr. A Gowtham	Associate Professor, Department of EECS, University of Cincinnati, Cincinnati, OHIO, USA	
5	Dr. A. Jayalakshmi	Professor & Head, Chairman-BoS, Dept. of CSE, PVPSIT	
6	Dr. M. V. Rama Krishna	Professor, Dept of CSE, PVPSIT	
7	Dr. S. Madhavi	Professor, Dept of CSE, PVPSIT	
8	Dr. B. Janakiramaiah	Professor, Dept of CSE, PVPSIT	
9	Dr. K. Koteswara Rao	Assoc. Professor, Dept of CSE, PVPSIT	
10	Ms. A. Ramana Lakshmi	Assoc. Professor, Dept of CSE, PVPSIT	
11	Mr. B N Swamy	Sr. Asst. Professor, Dept of CSE, PVPSIT	
12	Dr. J. Rama Devi	Sr. Asst. Professor, Dept of CSE, PVPSIT	
13	Dr. G. LalithaKumari	Sr. Asst. Professor, Dept of CSE, PVPSIT	
14	Dr. Lakshmi Ramani B	Asst. Professor, Dept of CSE, PVPSIT	
15	Dr. N V Ramana Gupta	Asst. Professor, Dept of CSE, PVPSIT	
16	Dr. S Phani Praveen	Asst. Professor, Dept of CSE, PVPSIT	

1. The Course Structure & detailed Syllabus of II B.Tech – CSE was presented through online meeting (**Google Meet**).
2. Having gone through the Course Structure and detailed Syllabus the members of BOS approved II B.Tech Course Structure and detailed Syllabus with the following suggestions/modifications:

The following resolutions were made:

- i. Include the following topics in Fundamentals of Digital Logic Design course of II Year-I Semester
 - Ripple Adder, BCD to Seven Segment Display Code Converter, Priority Encoder.
- ii. Introduce Object Oriented Programming through C++ course instead of Object Oriented Programming through JAVA in II Year - I Semester.
- iii. Include the following topics in Advanced Data Structures course of II Year-II Semester
 - Need for Randomizing Data Structures and Algorithms.
 - Search and Update Operations on Skip Lists.
- iv. Include Amortized analysis in Design and Analysis of Algorithms course of II Year-II Semester.
- v. Include Programming with JAVA as Skill Oriented Course in II Year – II Semester.
- vi. Resolved to shift the course Operating Systems from III Year – I Semester to II Year - II semester.
- vii. Resolved to shift the course Software Engineering from II Year – II Semester to III Year - I semester.
- viii. Include Topics related to Agile methodology in Software Engineering course of III Year – I semester.
- ix. Include Julia Programming as Skill Oriented Course in IV Year – I Semester.
- x. Encourage the students to crack the GATE with good score which adds value to their Curriculum Vitae.
- xi. Encourage students to become Job providers instead of Job seekers.

The meeting was concluded with vote of thanks.